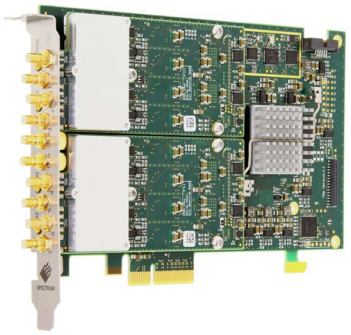
M2p.59xx-x4 - 16 bit general purpose Digitizer

- **Up to 125 MS/s on four or 80 MS/s on eight channels**
- **Ultra Fast PCI Express x4 interface**
- **Simultaneously sampling on all channels**
- **Separate dedicated 16 bit ADC and amplifier per channel**
- **6 input ranges: ±200 mV up to ±10 V**
- **512 MiSamples (1 GiByte) on-board memory**
- **Window, re-arm, OR/AND trigger**
- **Synchronization of up to 16 cards per system**
- **Features: Single-Shot, Streaming, Multiple Recording, Gated Sampling, ABA, Timestamps**
- **Direct data transfer to CUDA GPU using SCAPP option**

Speed	SNR	ENOB
5 MS/s	up to 86.0 dB	up to 14.0 LSB
20 MS/s	up to 81.0 dB	up to 13.2 LSB
40 MS/s	up to 75.3 dB	up to 12.2 LSB
80 MS/s	up to 74.2 dB	up to 12.0 LSB
125 MS/s	up to 73.3 dB	up to 11.8 LSB

Digital Pulse Generator FPGA Option: 4 independent digital pulses with programmable high, low, delay, loop on multi-purpose lines X0 to X3

SCAPP
Spectrum's CUDA Access – Parallel Processing



M2p series

- PCIe x4 Gen1 Interface
- Works with x4/x8/x16* PCIe slots
- Sustained streaming mode up to 700 MB/s**
- Half-length PCIe Form Factor

Operating Systems
- Windows 7 (SP1), 8, 10, 11 Server 2008 R2 and newer
- Linux Kernel 5.x, 6.x, 7.x
- Windows/Linux 32 and 64 bit

Programming Languages
- C, C++, C#, Python
- Julia, Java, VB.NET, Delphi
- IVI

Supported Software
- SBench 6
- MATLAB
- LabVIEW

	single-ended channels				true differential channels (non-isolated)		
Model	1 ch	2 ch	4 ch	8 ch	1 ch	2 ch	4 ch
M2p.5911-x4	5 MS/s	5 MS/s			5 MS/s	5 MS/s	
M2p.5912-x4	5 MS/s	5 MS/s	5 MS/s		5 MS/s	5 MS/s	
M2p.5916-x4	5 MS/s	5 MS/s	5 MS/s		5 MS/s	5 MS/s	5 MS/s
M2p.5913-x4	5 MS/s	5 MS/s	5 MS/s	5 MS/s	5 MS/s	5 MS/s	5 MS/s
M2p.5920-x4	20 MS/s	(OEM version)			20 MS/s	(OEM version)	
M2p.5921-x4	20 MS/s	20 MS/s			20 MS/s	20 MS/s	
M2p.5922-x4	20 MS/s	20 MS/s	20 MS/s		20 MS/s	20 MS/s	
M2p.5926-x4	20 MS/s	20 MS/s	20 MS/s		20 MS/s	20 MS/s	20 MS/s
M2p.5923-x4	20 MS/s	20 MS/s	20 MS/s	20 MS/s	20 MS/s	20 MS/s	20 MS/s
M2p.5930-x4	40 MS/s	(OEM version)			40 MS/s	(OEM version)	
M2p.5931-x4	40 MS/s	40 MS/s			40 MS/s	40 MS/s	
M2p.5932-x4	40 MS/s	40 MS/s	40 MS/s		40 MS/s	40 MS/s	
M2p.5936-x4	40 MS/s	40 MS/s	40 MS/s		40 MS/s	40 MS/s	40 MS/s
M2p.5933-x4	40 MS/s	40 MS/s	40 MS/s	40 MS/s	40 MS/s	40 MS/s	40 MS/s
M2p.5940-x4	80 MS/s				80 MS/s		
M2p.5941-x4	80 MS/s	80 MS/s			80 MS/s	80 MS/s	
M2p.5942-x4	80 MS/s	80 MS/s	80 MS/s		80 MS/s	80 MS/s	
M2p.5946-x4	80 MS/s	80 MS/s	80 MS/s		80 MS/s	80 MS/s	80 MS/s
M2p.5943-x4	80 MS/s	80 MS/s	80 MS/s	80 MS/s	80 MS/s	80 MS/s	80 MS/s
M2p.5960-x4	125 MS/s				125 MS/s		
M2p.5961-x4	125 MS/s	125 MS/s			125 MS/s	125 MS/s	
M2p.5962-x4	125 MS/s	125 MS/s	125 MS/s		125 MS/s	125 MS/s	
M2p.5966-x4	125 MS/s	125 MS/s	125 MS/s		125 MS/s	125 MS/s	125 MS/s
M2p.5968-x4	125 MS/s	125 MS/s	125 MS/s	80 MS/s	125 MS/s	125 MS/s	125 MS/s

General Information

The M2p.59xx series allows recording of up to eight Single-Ended channels or up to four differential channels both with sampling rates of up to 125 MS/s. These PCI Express cards offer outstanding A/D features both in resolution and speed.
The cards can be switched between Single-Ended inputs with a programmable offset and true differential inputs. If used in differential mode each two inputs are connected together reducing the number of available channels by half.
Importantly, the high-resolution 16-bit ADCs deliver sixteen times more resolution than digitizers using older 12-bit technology and 256 times more resolution than what is available from digital scopes that commonly use 8-bit ADCs.
All boards of the M2p.59xx series may use the whole installed on-board memory of up to 512 MSamples, completely for the currently activated number of channels.

*Some x16 PCIe slots are for the use of graphic cards only and can't be used for other cards. **Throughput measured with a motherboard chipset supporting a TLP size of 256 bytes.

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PHONE: +49 (0)4102-6956-0 · FAX: +49 (0)4102-6956-66 · E-MAIL: info@spec.de · INTERNET: www.spectrum-instrumentation.com
29.10.2026

Software Support

Windows drivers

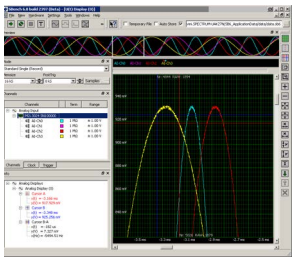
The cards are delivered with drivers for Windows 7, Windows 8, Windows 10 and Windows 11 (each 32 bit and 64 bit). Programming examples for Visual C++, Delphi, Visual Basic, VB.NET, C#, Python, Java, Julia and IVI are included.

Linux Drivers



All cards are delivered with full Linux support. Pre compiled kernel modules are included for the most common distributions like Fedora, Suse, Ubuntu LTS or Debian. The Linux support includes SMP systems, 32 bit and 64 bit systems, versatile programming examples for GNU C++, Python and Julia, as well as the possibility to get the kernel driver sources for your own compilation.

SBench 6

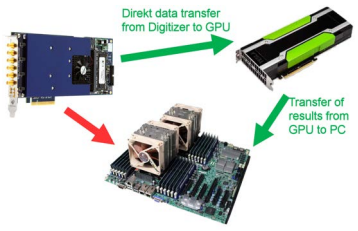


A base license of SBench 6, the easy-to-use graphical operating software for Spectrum cards, is included in the delivery. The base license makes it is possible to test the card, display acquired data and make some basic measurements. It's a valuable tool for checking the card's performance and assisting with the unit's initial setup. The cards also come with a demo license for the SBench 6 professional version. This license gives the user the opportunity to test the additional features of the professional version with their hardware. The professional version contains several advanced measurement functions, such as FFTs and X/Y display, import and export utilities as well as support for all acquisition modes including data streaming. Data streaming allows the cards to continuously acquire data and transfer it directly to the PC RAM or hard disk. SBench 6 has been optimized to handle data files of several GBytes. SBench 6 runs under Windows as well as Linux (KDE, GNOME and Unity) operating systems. A test version of SBench 6 can be downloaded directly over the internet and can run the professional version in a simulation mode without any hardware installed. Existing customers can also request a demo license for the professional version from Spectrum. More details on SBench 6 can be found in the SBench 6 data sheet.

Third-party products

Spectrum supports the most popular third-party software products such as LabVIEW or MATLAB. All drivers come with detailed documentation and working examples are included in the delivery.

SCAPP – CUDA GPU based data processing



For applications requiring high performance signal and data processing Spectrum offers SCAPP (Spectrum's CUDA Access for Parallel Processing). The SCAPP SDK allows a direct link between Spectrum digitizers, AWGs (AWG mode only, not usable for DDS mode) or Digital Data Acquisition Cards and CUDA based GPU cards. Once in the GPU users can harness the processing power of the GPU's multiple (up to 10000) processing cores and large (up to 48 GB) memories. SCAPP uses an RDMA (Linux only) process to send data at the full PCIe transfer speed to and from the GPU card. The SDK includes a set of examples for interaction between the Spectrum card and the GPU card and another set of CUDA parallel processing examples with easy building blocks for basic functions like filtering, averaging, data de-multiplexing, data conversion or FFT. All the software is based on C/C++ and can easily be implemented, expanded and modified with normal programming skills.

Hardware features and options

PCI Express x4



The M2p series cards use a PCI Express x4 Gen1 connection. They can be used in PCI Express x4, x8 and x16 slots with hosts supporting Gen1, Gen2, Gen3, Gen4 or Gen5. The maximum sustained data transfer rate is more than 700 MByte/s (read direction) or 700 MByte/s (write direction) per slot. Physically supported slots that are electrically connected with only x1 or x2 can also be used with the M2p series cards, but with reduced data transfer rates.

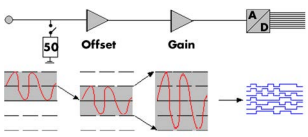
Connections



The cards are equipped with SMB connectors for the analog signals as well as for the external trigger and clock input. In addition, there are four MMCX connectors: one multi-function output (X0) and three multi-function I/O connectors (X1, X2, X3). These multi-function connectors can be individually programmed to perform different functions:

- Clock output (X0 only)
- Trigger output
- Status output (armed, triggered, ready, ...)
- Synchronous digital inputs, being stored inside the analog data samples
- Asynchronous I/O lines
- Logic trigger inputs

Input Amplifier



The analog inputs can be adapted to real world signals using a wide variety of settings that are individual for each channel. By using software commands the input termination can be changed between 50 Ohm and 1 MOhm, one can select a matching input range and the signal offset can be compensated for.

Differential inputs

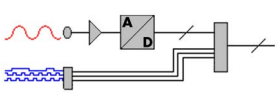
With a simple software command the inputs can individually be switched from single-ended (in relation to ground) to differential by combining each two single-ended inputs to one differential input. When the inputs are used in differential mode the A/D converter measures the difference between two lines with relation to system ground.

Calibration on-board/factory/ISO/IEC 17025

All of the channels are calibrated in factory before the board is shipped. To compensate for different variations like PC power supply, temperature and aging, the software driver provides routines for an automatic on-board offset and gain calibration of all input ranges. All the cards contain a high precision on-board calibration reference.

Depending on the QM needs the card can shipped to factory for a complete re-calibration against external traceable calibration sources. As an option a ISO/IEC17025-compliant certificate is available with new cards and after recalibration.

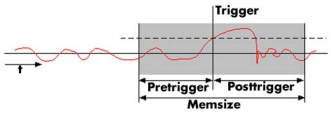
Digital inputs



This option acquires additional synchronous digital channels phase-stable with the analog data. As default a maximum of 3 additional

digital inputs are available on the front plate of the card using the multi-purpose I/O lines. An additional option offers 16 more digital channels.

Ring buffer mode



The ring buffer mode is the standard mode of all oscilloscope instruments. Digitized data is continuously written into a ring memory until a trigger event is detected. After the trigger, post-trigger samples are recorded and pre-trigger samples can also be stored. The number of pre-trigger samples available simply equals the total ring memory size minus the number of post trigger samples.

FIFO mode

The FIFO or streaming mode is designed for continuous data transfer between the card and the PC memory. When mounted in a PCI Express x4 Gen1 interface both, read and write streaming speeds of up to 700 MByte/s are possible. The control of the data stream is done automatically by the driver on interrupt request basis. The complete installed on-board memory is used to buffer the data, making the continuous streaming process extremely reliable.

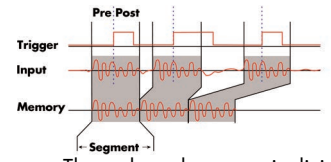
Channel trigger

The digitizers offer a wide variety of trigger modes. These include a standard triggering mode based on a signals level and slope, like that found in most oscilloscopes. It is also possible to define a window mode, with two trigger levels, that enables triggering when signals enter or exit the window. Each input has its own trigger circuit which can be used to setup conditional triggers based on logical AND/OR patterns. All trigger modes can be combined with a re-arming mode for accurate trigger recognition even on noisy signals.

External trigger input

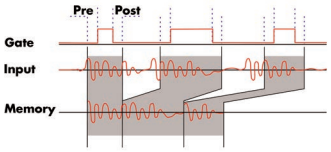
All boards can be triggered using an external analog or digital signal. The external trigger input has one comparator that can be used for standard edge and level triggers.

Multiple Recording



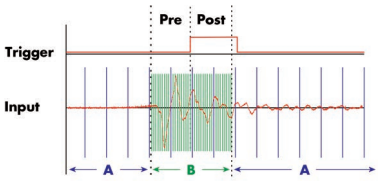
The Multiple Recording mode allows the recording of several trigger events with an extremely short re-arming time. The hardware doesn't need to be restarted in between. The on-board memory is divided in several segments of the same size. Each of them is filled with data if a trigger event occurs. Pre- and posttrigger of the segments can be programmed. The number of acquired segments is only limited by the used memory and is unlimited when using FIFO mode.

Gated Sampling



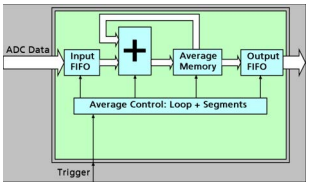
The Gated Sampling mode allows data recording controlled by an external gate signal. Data is only recorded if the gate signal has a programmed level. In addition a pre-area before start of the gate signal as well as a post area after end of the gate signal can be acquired. The number of gate segments is only limited by the used memory and is unlimited when using FIFO mode.

ABA mode



The ABA mode combines slow continuous data recording with fast acquisition on trigger events. The ABA mode works like a slow data logger combined with a fast digitizer. The exact position of the trigger events is stored as timestamps in an extra memory.

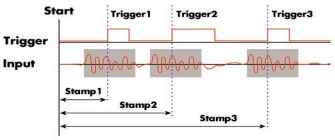
Firmware Option Block Average



The Block Average Module improves the fidelity of noisy repetitive signals. Multiple repetitive acquisitions with very small dead-time are accumulated and averaged. Random noise is reduced by the averaging process improving the visibility of the repetitive signal. The complete averaging process is done inside the FPGA of the digitizer, generating no CPU load at all. The amount of data is greatly decreased as well as the needed transfer bandwidth is heavily reduced.

Please see separate data sheet for details on the firmware option.

Timestamp

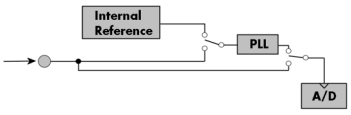


The timestamp function writes the time positions of the trigger events in an extra memory. The timestamps are relative to the start of recording, a defined zero time, externally synchronized to a radio clock, an IRIG-B a GPS receiver. Using the external synchronization gives a precise time relation for acquisitions of systems on different locations.

External clock input and output

Using a dedicated connector a sampling clock can be fed in from an external system. Additionally it's also possible to output the internally used sampling clock on a separate connector to synchronize external equipment to this clock.

Reference clock



The option to use a precise external reference clock (typically 10 MHz) is necessary to synchronize the instrument for high-quality measurements with external equipment (like a signal source). It's also possible to enhance the stability of the sampling clock in this way. The driver automatically generates the requested sampling clock from the fed in reference clock.

Star-Hub



The Star-Hub is an additional module allowing the phase stable synchronization of up to 16 boards in one system. Two versions are available: one with up to 6 cards and the large version supports up to 16 cards in one system. Both versions can be mounted in two different ways, to either extend the cards length to ¾ PCIe length occupying one slot, or extend its width to two slots whilst keeping the ½ PCIe length.



Independent of the number of boards there is no phase delay between the channels. The Star-Hub distributes trigger and clock information between all boards. As a result all connected boards are running with the same clock and the same trigger. All trigger sources can be combined with OR/AND. For digitizers that means all channels of all cards to be trigger source at the same time.

Multi-Purpose I/O 4 Standard + 16 Option



As standard each card has 4 multi-purpose I/O lines (3 x I/O and 1 x Output). As an option a piggy-back module carries additional 16 mutli-purpose I/O lines making up to 19 digtal inputs or 20 digital outputs.

This option is available with SMB connectors or with FX/2 connector for flat-ribbon cable, with pin-compatibility with previous hardware versions.

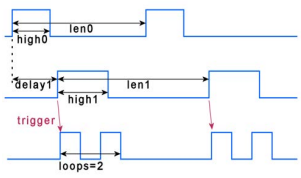
All I/O lines can be used for synchronous digital data acquisition (digitizer), synchronous digital data output/marker output (AWG), asynchronous digital I/O, can carry additional status information or can be used as trigger inputs

External Amplifiers



For the acquisition of extremely small voltage levels with a high bandwidth a series of external amplifiers is available. Each of the one channel amplifiers is working with a fixed input impedance and allows - depending on the bandwidth - to select different amplification levels between x10 (20 dB) up to x1000 (60 dB). Using the external amplifiers of the SPA series voltage levels in the uV and mV area can be acquired.

Firmware Option Digital Pulse Generator



The digital pulse generator option adds 4 internal independent digital pulse generators with programmable duty cycle, output frequency, delay and number of loops. These digital pulse generators can be triggered by software, hardware trigger or can trigger each other allowing to form complex pulse schemes to drive external equipment or experiments. The digital pulse generators can be output on the existing multi-XIO lines (X0, X1, ...), to trigger other pulse generators or can be used to trigger the instrument's main trigger internally. Time resolution of the pulse generator depends on the cards type and the selected sampling rate and can be found in the technical data section.
The pulse generator option is a firmware option and can be later installed on all shipped cards.

Technical Data

Definitions

Specifications (spec)
Figures are valid for products stored for at least 2 hours inside the specified operating temperature range, after a 30 minute warm-up, after running an on-board calibration and with properly cooled products. Data published in this document are specifications (spec) only where specifically indicated. Figures marked with (spec) are calibrated to the specification during production and during factory calibrations.

Typical (typ)
Figures marked with (typ) are a characteristic performance that most of the manufactured instruments will meet due to design. Typical figures are measured during production but cannot be calibrated.

Measured (meas)
A figure measured during development to communicate expected performance. The figure is measured in lab environment with an environmental temperature between 20°C and 25°C and an altitude of less than 100 m.

Nominal (nom)
The value of this figure is determined by design and is not measured nor calibrated.

Analog Inputs

Resolution (nom)		16 bit (can be reduced to acquire simultaneous digital inputs)
Input Range (nom)	software programmable	±200 mV, ±500 mV, ±1 V, ±2 V, ±5 V, ±10 V
Input Type	software programmable	Single-ended or True Differential
Input Offset single-ended (nom)	software programmable	programmable to ±100% of input range in steps of 1%
ADC Differential non linearity (nom)	DNL, ADC only	591x: ±0.2/±0.8 LSB (typ./max.) 592x: ±0.2/±0.8 LSB (typ./max.) 593x, 8x3: ±0.5/±0.9 LSB (typ./max.) 594x: ±0.5/±0.9 LSB (typ./max.) 596x, 8x6: ±0.5/±0.9 LSB (typ./max.
ADC Integral non linearity (nom)	INL, ADC only	591x: ±1.0/±2.3 LSB (typ./max.) 592x: ±1.0/±2.3 LSB (typ./max.) 593x, 803, 813: ±2.0/±7.5 LSB (typ./max.) 594x: ±2.0/±7.5 LSB (typ./max.) 596x, 806, 816: ±2.0/±7.5 LSB (typ./max.)
Offset error (spec)	full speed, DC signal	≤ 0.1% of range
Gain error Single-Ended (spec)	full speed, DC signal	≤ 0.1% of reading
Gain error Differential (spec)	full speed, DC signal	≤ 0.15% of reading
Offset temperature drift (meas)	after warm-up and calibration	typical 5 ppm/K of range
Gain temperature drift (meas)	after warm-up and calibration	typical 45 ppm/K of reading
AC accuracy (typ)	1 kHz signal	≤ 0.3% of reading
AC accuracy (typ)	50 kHz signal	≤ 0.5% of reading
Crosstalk: Signal 1 MHz, 50 Ω (meas)	range ≤ ±1V range ≥ ±2V	≤ 95 dB on adjacent channels ≤ 90 dB on adjacent channels
Crosstalk: Signal 10 MHz, 50 Ω (meas)	range ≤ ±1V range ≥ ±2V	≤ 87 dB on adjacent channels ≤ 85 dB on adjacent channels
Analog Input impedance (nom)	software programmable	50 Ω /1 MΩ || 30 pF
Analog input coupling (nom)	fixed	DC
Over voltage protection (nom)	range ≤ ±1V	±5 V (1 MΩ), 3.5 Vrms (50 Ω)
Over voltage protection (nom)	range ≥ ±2V	±50 V (1 MΩ), 5 Vrms (50 Ω)
Anti-Aliasing Filter, digital filtering active (nom)	591x (5 MS/s)	Digital Anti-Aliasing filter at 40% of sampling rate. Examples: 5 MS/s sampling rate -> anit-aliasing filter at 2 MHz 1 MS/s sampling rate -> anti-aliasing filter at 400 kHz
Anti-Aliasing Filter (nom)	591x (5 MS/s) 592x (20 MS/s) 593x (40 MS/s) 594x (80 MS/s) 596x (125 MS/s)	fixed 2.5 MHz 3rd order butterworth alike fixed 10 MHz 3rd order butterworth alike fixed 20 MHz 3rd order butterworth alike fixed 40 MHz 3rd order butterworth alike fixed 60 MHz 3rd order butterworth alike
CMRR, Common Mode Rejection Ratio (meas)	range ≤ ±1V	100 kHz: 75 dB, 1 MHz: 60 dB, 10 MHz: 40 dB
CMRR, Common Mode Rejection Ratio (meas)	range ≥ ±2V	100 kHz: 55 dB, 1 MHz: 52 dB, 10 MHz: 50 dB
Common Mode Voltage Range Differential Input (nom)	Input Range VCM (1 MΩ termination) VCM (50 Ω termination)	±200 mV ±500 mV ±1 V ±2 V ±5 V ±10 V ±900 mV ±2.25 V ±2.25 V ±9 V ±22.5 V ±22.5 V ±900 mV ±2.25 V ±2.25 V ±3.5 V ±3.5 V ±3.5 V
Channel selection, single-ended inputs	software programmable	1, 2, 4 or 8 channels (maximum is model dependent)
Channel selection, true differential inputs	software programmable	1, 2 or 4 channels (maximum is model dependent)
Calibration	Internal	Self-calibration is done on software command and corrects against the on-board references. Self-calibration should be issued after warm-up time.
Calibration	External	External calibration calibrates the onboard references used in self-calibration. All calibration constants are stored in non-volatile memory. Optional ISO/IEC17025-compliant certificate. A yearly external calibration is recommended.

Trigger

Available trigger modes	software programmable	Channel Trigger, External, Software, Window, Pulse, Re-Arm, Or/And, Delay
Channel trigger level resolution (nom)	software programmable	16 bit
Trigger edge	software programmable	Rising edge, falling edge or both edges
Trigger pulse width	software programmable	0 to [4Gi - 1] samples in steps of 1 sample
Trigger delay	software programmable	0 to [4Gi - 1] samples in steps of 1 samples
Trigger holdoff Multi, ABA, Gate	software programmable	0 to [4Gi - 1] samples in steps of 1 samples
Multi, ABA, Gate: re-arming time (nom)		< 40 samples (+ programmed pretrigger + programmed holdoff)
Trigger input delay to internal trigger (meas)		43 sample clocks
Trigger input to trigger output delay (meas)	Firmware version ≥ V29	45 sample clocks
Pretrigger at Multi, ABA, Gate, FIFO	software programmable	8 up to [32 KiSamples / number of active channels] in steps of 8
Posttrigger	software programmable	8 up to [8Gi - 4] samples in steps of 8 (defining pretrigger in standard scope mode)
Memory depth	software programmable	16 up to [installed memory / number of active channels] samples in steps of 8

Trigger edge	software programmable	Rising edge, falling edge or both edges	
Multiple Recording/ABA segment size	software programmable	8 up to [installed memory / number of active channels] samples in steps of 8	
Internal/External trigger accuracy		1 sample	
Timestamp modes	software programmable	Standard, Startreset, external reference clock on X1 (e.g. PPS from GPS, IRIG-B)	
Data format		Std., Startreset: 64 bit counter, increments with sample clock (reset manually or on start)	
		RefClock: 24 bit upper counter (increment with RefClock) 40 bit lower counter (increments with sample clock, reset with RefClock)	
Extra data	software programmable	none, acquisition of X1/X2/X3 inputs at trigger time, trigger source (for OR trigger)	
Size per stamp		128 bit = 16 bytes	
External trigger		**Ext**	**X1, X2, X3**
External trigger type		Single level comparator	3.3V LVTTL logic inputs
External trigger impedance (typ)	software programmable	50 Ω / 5 kΩ	For electrical specifications refer to „Multi Purpose I/O lines" section.
External trigger input level (nom)		±5 V (5 kΩ), ±2.5 V (50 Ω),	
External trigger over voltage protection (nom)		±20 V (5 kΩ), 5 Vrms (50 Ω)	
External trigger sensitivity, min signal swing (nom)		200 mVpp	
External trigger level (nom)	software programmable	±5 V in steps of 10 mV	
External trigger bandwidth (typ)	50 Ω 5 kΩ	DC to 400 MHz DC to 300 MHz	n.a. DC to 125 MHz
Minimum external trigger pulse width		≥ 2 samples	≥ 2 samples
Minimum pause between external triggers		≥ 2 samples	≥ 2 samples
Resulting max. detectable trigger frequency		[Current Samplerate]/4	[Current Samplerate]/4

Multi Purpose I/O lines

Number of multi purpose output lines		one, named X0	
Number of multi purpose input/output lines		three, named X1, X2, X3	
Multi Purpose line		**X0**	**X1, X2, X3**
Input: available signal types	software programmable	n.a.	Synchronous Digital-In, Asynchronous Digital-In, Timestamp Reference Clock, Logic trigger
Input: signal levels (nom)		n.a.	3.3 V LVTTL (Low ≤ 0.8 V, High ≥ 2.0 V)
Input: impedance (nom)		n.a.	10 kΩ to 3.3 V
Input: maximum voltage level (nom)		n.a.	-0.5 V to +4.0 V
Input: maximum bandwidth (nom)		n.a.	half of sampling rate
Output: available signal types	software programmable	Run-, Arm-, Trigger-Output, Asynchronous Digital-Out, ADC Clock Output Digital Pulse Generator (option)	Run-, Arm-, Trigger-Output, Asynchronous Digital-Out Digital Pulse Generator (option)
Output: impedance (nom)		50 Ω	
Output: drive strength (nom)		Capable of driving 50 Ω loads, maximum drive strength ±48 mA	
Output: type / signal levels (nom)		3.3V LVTTL, TTL compatible for high impedance loads	
Output: update rate, synchronous modes (nom)		sampling clock	

Option M2p.xxxx-PulseGen

Number of internal pulse generators	4
Number of pulse generator output lines	4 (Existing multi-purpose outputs X0 to X3)
Time resolution of pulse generator	Selected Sampling Rate, max is 125 MS/s (8 ns)
Programmable output modes	Single-shot, multiple repetitions on trigger, gated
Programmable trigger sources	Software, Card Trigger, Other Pulse Generator, XIO lines.
Programmable trigger gate	None, ARM state, RUN state
Programmable length (frequency)	2 to 4Gi samples in steps of 1 (32 bit)
Programmable width (duty cycle)	1 to 4Gi samples in steps of 1 (32 bit)
Programmable delay	0 to 4Gi samples in steps of 1 (32 bit)
Programmable loops	0 to 4Gi samples in steps of 1 (32 bit) with 0 = infinite loops
Output level of digital pulse generators	Please see section of multi-purpose I/O lines.

Option M2p.xxxx-DigFX2 / M2p.xxxx-DigSMB common

Input: signal levels (meas)		3.3 V LVTTL
Input: impedance (meas)		10 kΩ to 3.3 V
Input: maximum voltage level (nom)		-0.5 V to +4.0 V
Input: maximum bandwidth (meas)		125 MHz
Input: available signal types	software programmable	Synchronous Digital-In (M2p.59xx only), Asynchronous Digital-In
Output: available signal types	software programmable	Run-, Arm-, Trigger-Output, Synchronous Digital-Out (M2p.65xx only), Asynchronous Digital-Out
Output: update rate synchronous modes (meas)		sampling clock
Output: type / signal levels (meas)		3.3V LVTTL, TTL compatible for high impedance loads

Option M2p.xxxx-DigFX2 specific

Number of additional multi-purpose I/O lines	16 (X4 to X19)
Card width with installed option	Requires one additional slot left of the main card's bracket, on „solder side" of the PCIe card
Connector	1 x 40 pole half pitch (Hirose FX2 series, one adapter cable to IDC connector in standard 2.54mm pitch included (Cab-d40-xx-xx). 4 x SMB male, (jumper selectable between FX2/SMB for: X12, X13, X18 and X19)) Connector on card: Hirose FX2B-40PA-1.27DSL Flat ribbon cable connector: Hirose FX2B-40SA-1.27R
Output: impedance (meas)	FX2: 90 Ω , SMB: 50 Ω
Output: drive strength (meas)	Capable of driving 90 Ω loads (FX2), 50 Ω loads (SMB), maximum drive strength ±48 mA
Compatibility	Pinning compatible with M2i.xxxx-dig option and M2i.70xx connectors

Option M2p.xxxx-DigSMB specific

Number of additional multi purpose I/O lines	16 (X4 to X19)
Card width with installed option	Requires one additional slot left of the main card's bracket, on „solder side" of the PCIe card
Connectors on bracket	10 x SMB male (X4 to X13)
Internal connectors	6 x SMB male (X14 to X19)
Output: impedance (meas)	50 Ω
Output: drive strength (meas)	Capable of driving 50 Ω loads, maximum drive strength ±48 mA

Clock

Clock Modes	software programmable	internal PLL, external clock, external reference clock, sync
Internal clock range PLL mode	software programmable	see „Clock Limitations and Bandwidth" table below
Internal clock accuracy (spec)	after warm-up	≤ ±1.0 ppm (at time of calibration in production)
Internal clock aging (spec)		≤ ±0.5 ppm / year
PLL clock setup granularity		1 Hz
External reference clock range (nom)	software programmable	128 kHz up to 125 MHz
Direct external clock to internal clock delay (meas)	single card only	4.3 ns
Direct external clock range		see „Clock Limitations and Bandwidth" table below
Direct external clock minimum LOW/HIGH time		see „Clock Limitations and Bandwidth" table below
External clock type		Single level comparator
External clock input operating range (nom)		±5 V (5 kΩ), ±2.5 V (50 Ω),
External clock input impedance (typ)	software programmable	50 Ω / 5 kΩ
External clock over voltage protection (nom)		±20 V (5 kΩ), 5 Vrms (50 Ω)
External clock sensitivity, min signal swing (nom)		200 mVpp
External clock comparator level (nom)	software programmable	±5 V in steps of 1mV
External clock edge		rising edge used
External reference clock input duty cycle (nom)		45% - 55%
Clock output electrical specification		Available via Multi Purpose output X0. Refer to „Multi Purpose I/O lines" section.
Synchronization clock multiplier „N" for different clocks on synchronized cards	software programmable	N being a multiplier (1, 2, 3, 4, 5, ... Max) of the card with the currently slowest sampling clock. The card maximum (see „Clock Limitations and Bandwidth" table below) must not be exceeded.
ABA mode clock divider for slow clock	software programmable	8 up to (64Ki - 8) in steps of 8
Channel to channel skew on one card (meas)		< 200 ps
Skew between star-hub synchronized cards (meas)		< 100 ps

Block Average Signal Processing Option M2p.59xx/DN2.59x/DN6.59x/DN2.80x/DN2.81x Series

Minimum Waveform Length		16 samples
Minimum Waveform Step size		8 samples
Maximum Waveform Length	1 channel active	64 KiSamples
	2 channels active	32 KiSamples
	4 channels active	16 KiSamples
	8 channels active	8 KiSamples
Minimum Number of Averages		2
Maximum Number of Averages		64
Data Output Format	fixed	32 bit signed integer
Re-Arming Time between waveforms (nom)		40 samples (+ programmed pretrigger + programmed holdoff)
Re-Arming Time between end of average to start of next average (nom)		3 times the programmed segment length's (L) acquisition time: t = 3 * SegmentLen * ActiveChannels / Samplerate

Connectors

Analog		SMB male (one for each single-ended input/output)	Cable-Type: Cab-3f-xx-xx
Trigger Input		SMB male	Cable-Type: Cab-3f-xx-xx
Clock Input		SMB male	Cable-Type: Cab-3f-xx-xx
Standard Multi Purpose I/O		MMCX female (4 lines)	Cable-Type: Cab-1m-xx-xx
Option M2p.xxxx-DigSMB	on extra bracket	SMB male	Cable-Type: Cab-3f-xx-xx
Option M2p.xxxx.DigFX2	on extra bracket	40-pole half pitch (Hirose FX2)	Cable-Type: Cab-d40-xx-xx

Connection Cycles

All connectors have an expected lifetime as specified below. Please avoid to exceed the specified connection cycles or use connector savers.

SMB connector (nom)	500 connection cycles
MMCX connecto (nom)r	500 connection cycles
Hirose FX2 connecto (nom)r	500 connection cycles
PCIe connector (nom)	50 connection cycles

Environmental and Physical Details

Dimension (Single Card) type M2p.65x3, M2p.65x8, M2p.654x or M2p.657x	8 channel AWG or High power AWG	L x H x W: 168 mm (½ PCIe length) x 107 mm x 30 mm. Requires one additional slot right of the main card's bracket, on „component side" of the PCIe card.
Dimension (all other single cards)		L x H x W: 168 mm (½ PCIe length) x 107 mm x 20 mm (single slot width)
Dimension (with -SH6tm or -SH16tm installed)		Extends W by 1 slot right of the main card's bracket, on „component side" of the PCIe card.
Dimension (with -SH6ex or -SH16ex installed)		Extends L to 245 mm (¾ PCIe length) at the back of the PCIe card
Dimension (with -DigSMB or -DigFX2 installed)		Extends W by 1 slot left of the main card's bracket, on „solder side" of the PCIe card.
Weight (M2p.59xx, M2p.75xx series)	maximum	215 g
Weight (M2p.65x0, M2p.65x1, M2p.65x6 series)	maximum	195 g
Weight (M2p.65x3, 65x8, 654x, 657x series)	maximum	305 g
Weight (Star-Hub Option -SH6ex, -SH6tm)	including 6 sync cables	65 g
Weight (Star-Hub Option -SH16ex, -SH16tm)	including 16 sync cables	90 g
Weight (Option -DigSMB)		50 g
Weight (Option -DigFX2)		60 g
Warm up time (meas)		30 minutes
Operating temperature (nom)		0 °C to 40 °C
Storage temperature (nom)		-10 °C to 70 °C
Humidity (nom)		10% to 90%
Dimension of packing	1 or 2 cards	470 mm x 250 mm x 130 cm
Volume weight of packing	1 or 2 cards	4 kg

PCI Express specific details

PCIe slot type	x4, Generation 1 (Gen1)
PCIe slot compatibility (physical)	x4, x8, x16
PCIe slot compatibility (electrical)	x1, x2, x4, x8, x16 with PCIe Gen1, Gen2, Gen3, Gen4 or Gen5
Sustained streaming mode (meas) (Card-to-System: M2p.59xx or M2p.75xx)	> 700 MB/s (measured with a chipset supporting a TLP size of 256 bytes, using PCIe x4 Gen1)
Sustained streaming mode (meas) (System-to-Card: M2p.65xx or M2p.75xx)	> 700 MB/s (measured with a chipset supporting a TLP size of 256 bytes, using PCIe x4 Gen1)

Certification, Compliance, Warranty

Conformity Declaration	EN 17050-1:2010	General Requirements
EU Directives	2014/30/EU	EMC - Electromagnetic Compatibility
	2014/35/EU	LVD - Electrical equipment designed for use within certain voltage limits
	2011/65/EU	RoHS - Restriction of the use of certain hazardous substances in electrical and electronic equipment
	2006/1907/EC	REACH - Registration, Evaluation, Authorisation and Restriction of Chemicals
	2012/19/EU	WEEE - Waste from Electrical and Electronic Equipment
Compliance Standards	EN 61010-1: 2010	Safety regulations for electrical measuring, control, regulating and laboratory devices - Part 1: General requirement
	EN 61187:1994	Electrical and electronic measuring equipment - Documentation
	EN 61326-1:2021	Electrical equipment for measurement, control and laboratory use
	EN 61326-2-1:2021	EMC requirements - Part 1: General requirements
		EMC requirements - Part 2-1: Particular requirements - Test configurations, operational conditions and performance criteria for sensitive test and measurement equipment for EMC unprotected applications
	EN IEC 63000:2018	Technical documentation for the assessment of electrical and electronic products with respect to the restriction of hazardous substances
Product warranty	5 years starting with the day of delivery	
Software and firmware updates	Life-time, free of charge	

Power Consumption

	3.3V	12V	Total
M2p.59x0, 59x1, 59x2 (meas)	0.1 A	1.1 A	13.6 W
M2p.59x3, 59x6, 59x8 (meas)	0.1 A	1.5 A	18.4 W

MTBF

MTBF (typ)	100000 hours

Clock Limitations and Bandwidth

	M2p.591x, DN2.591-xx DN6.591-xx	M2p.592x, DN2.592-xx DN6.592-xx	M2p.593x DN2.593-xx DN6.593-xx DN2.803-xx DN2.813-xx	M2p.594x	M2p.596x DN2.596-xx DN6.596-xx DN2.806-xx DN2.816-xx
max internal clock (non-synchronized cards)	5 MS/s	20 MS/s	40 MS/s	80 MS/s	125 MS/s
min internal clock (non-synchronized cards)	1 kS/s	1 kS/s	1 kS/s	1 kS/s	1 kS/s
max internal clock (cards synchronized via star-hub)	5 MS/s	20 MS/s	40 MS/s	80 MS/s	125 MS/s
min internal clock (cards synchronized via star-hub)	128 kS/s	128 kS/s	128 kS/s	128 kS/s	128 kS/s
max direct external clock	5 MS/s	20 MS/s	40 MS/s	80 MS/s	125 MS/s
min direct external clock	1 MS/s	1 MS/s	1 MS/s	1 MS/s	1 MS/s
min direct external clock LOW time	25 ns	25 ns	4 ns	4 ns	4 ns
min direct external clock HIGH time	25 ns	25 ns	4 ns	4 ns	4 ns
-3 dB analog input bandwidth (typ)	> 2.0 MHz	> 10 MHz	> 20 MHz	> 40 MHz	> 60 MHz
-3 dB analog input bandwidth, digital filter de-activated (typ)	> 2.5 MHz	n.a.	n.a.	n.a.	n.a.

RMS Noise Level (Zero Noise), typical figures

M2p.591x, DN2.591-xx, DN6.591-xx digital filtering active

Input Range	±200 mV		±500 mV		±1 V		±2 V		±5 V		±10 V	
Voltage resolution	6.1 µV		15.3 µV		30.5 µV		61.0 µV		152.6 µV		305.2 µV	
50 Ω (typ)	<1.5 LSB	<10 µV	<1.2 LSB	<19 µV	<1.0 LSB	<31 µV	<3.0 LSB	<183 µV	<1.6 LSB	<245 µV	<1.2 LSB	<367 µV
1 MΩ (typ)	<1.5 LSB	<10 µV	<1.2 LSB	<19 µV	<1.0 LSB	<31 µV	<3.0 LSB	<183 µV	<1.6 LSB	<245 µV	<1.2 LSB	<367 µV

M2p.592x, DN2.592-xx, DN6.592-xx

Input Range	±200 mV		±500 mV		±1 V		±2 V		±5 V		±10 V	
Voltage resolution	6.1 µV		15.3 µV		30.5 µV		61.0 µV		152.6 µV		305.2 µV	
50 Ω (typ)	<4.0 LSB	<25 µV	<2.6 LSB	<40 µV	<2.1 LSB	<65 µV	<4.3 LSB	<263 µV	<2.6 LSB	<397 µV	<2.1 LSB	<641 µV
1 MΩ (typ)	<4.5 LSB	<28 µV	<3.0 LSB	<46 µV	<2.5 LSB	<107 µV	<4.5 LSB	<275 µV	<3.0 LSB	<458 µV	<2.5 LSB	<763 µV

M2p.593x, DN2.593-xx, DN6.593-xx, DN2.803-xx, DN2.813-xx

Input Range	±200 mV		±500 mV		±1 V		±2 V		±5 V		±10 V	
Voltage resolution	6.1 µV		15.3 µV		30.5 µV		61.0 µV		152.6 µV		305.2 µV	
50 Ω (typ)	<6.0 LSB	<37 µV	<5.0 LSB	<77 µV	<4.5 LSB	<138 µV	<6.5 LSB	<397 µV	<5.0 LSB	<763 µV	<4.5 LSB	<1.4 mV
1 MΩ (typ)	<6.5 LSB	<40 µV	<5.0 LSB	<77 µV	<4.5 LSB	<138 µV	<6.5 LSB	<397 µV	<5.0 LSB	<763 µV	<4.5 LSB	<1.4 mV

M2p.594x

Input Range	±200 mV		±500 mV		±1 V		±2 V		±5 V		±10 V	
Voltage resolution	6.1 µV		15.3 µV		30.5 µV		61.0 µV		152.6 µV		305.2 µV	
50 Ω (typ)	<7.0 LSB	<43 µV	<5.5 LSB	<85 µV	<4.5 LSB	<138 µV	<7.5 LSB	<458 µV	<5.5 LSB	<840 µV	<4.5 LSB	<1.4 mV
1 MΩ (typ)	<7.5 LSB	<46 µV	<5.8 LSB	<89 µV	<4.5 LSB	<138 µV	<7.7 LSB	<470 µV	<5.8 LSB	<886 µV	<4.5 LSB	<1.4 mV

M2p.596x, DN2.596-xx, DN6.596-xx, DN2.806-xx, DN2.816-xx

Input Range	±200 mV		±500 mV		±1 V		±2 V		±5 V		±10 V	
Voltage resolution	6.1 µV		15.3 µV		30.5 µV		61.0 µV		152.6 µV		305.2 µV	
50 Ω (typ)	<9.0 LSB	<55µV	<6.8 LSB	<104 µV	<5.5 LSB	<168 µV	<9.0 LSB	<550 µV	<6.8 LSB	<1.1 mV	<5.5 LSB	<1.7 mV
1 MΩ (typ)	<9.5 LSB	<58µV	<7.1 LSB	<109 µV	<5.5 LSB	<168 µV	<9.5 LSB	<580 µV	<7.1 LSB	<1.1 mV	<5.5 LSB	<1.7 mV

Dynamic Parameters, typical figures

M2p.591x, DN2.591-xx, DN6.591-xx digital filtering active

Test - sampling rate	5 MS/s							
Input Range	±200 mV		±500 mV		±1 V		±2 V	
Test Signal Frequency	20 kHz	1 MHz	20 kHz	1 MHz	20 kHz	1 MHz	20 kHz	1 MHz
SNR (typ)	≥ 83.5 dB	≥ 82.8 dB	≥ 85.0 dB	≥ 84.9 dB	≥ 86.2 dB	≥ 85.7 dB	n.a.	n.a.
THD (typ)	(≤ 84.4 dB)	≤ -93.5 dB	(≤ 86.3 dB)	≤ -93.1 dB	(≤ 86.9 dB)	≤ -91.8 dB	n.a.	n.a.
SFDR (typ), excl. harm.	≥ 103.0 dB	≥ 103.0 dB	≥ 104.0 dB	≥ 107.0 dB	≥ 103.0 dB	≥ 107.0 dB	n.a.	n.a.
ENOB based on SNR (typ)	≥ 13.6 LSB	≥ 13.4 LSB	≥ 13.8 LSB	≥ 13.8 LSB	≥ 14.0 LSB	≥ 13.9 LSB	n.a.	n.a.
ENOB based on SINAD (typ)	≥ 13.1 LSB	≥ 13.4 LSB	≥ 13.4 LSB	≥ 13.7 LSB	≥ 13.6 LSB	≥ 13.8 LSB	n.a.	n.a.

M2p.591x, DN2.591-xx, DN6.591-xx digital filtering active

Test - sampling rate	3 MS/s		1 MS/s		500 kS/s		200 kS/s	
Input Range	±200 mV	±1 V	±200 mV	±1 V	±200 mV	±1 V	±200 mV	±1 V
Test Signal Frequency	20 kHz		20 kHz		20 kHz		20 kHz	
Input bandwidth due to digital filter	1.2 MHz		400 kHz		200 klHz		80 kHz	
SNR (typ)	≥ 85.3 dB	≥ 86.6 dB	≥ 87.2 dB	≥ 89.1 dB	≥ 86.2 dB	≥ 89.7 dB	≥ 86.4 dB	≥ 89.4 dB
THD (typ)	(≤ 88.9 dB)	(≤ -88.5 dB)	(≤ 86.4 dB)	(≤ -88.6 dB)	(≤ 86.9 dB)	(≤ -90.8 dB)	(≤ 89.7 dB)	(≤ -93.8 dB)
SFDR (typ), excl. harm.	≥ 103.1 dB	≥ 103.6 dB	≥ 102.8 dB	≥ 105.6 dB	≥ 103.1 dB	≥ 103.1 dB	≥ 103.1 dB	≥ 103.5 dB
ENOB based on SNR (typ)	≥ 13.9 LSB	≥ 14.1 LSB	≥ 14.2 LSB	≥ 14.5 LSB	≥ 14.0 LSB	≥ 14.6 LSB	≥ 14.1 LSB	≥ 14.6 LSB
ENOB based on SINAD (typ)	≥ 13.5 LSB	≥ 13.7 LSB	≥ 13.6 LSB	≥ 14.0 LSB	≥ 13.6 LSB	≥ 14.2 LSB	≥ 13.8 LSB	≥ 14.3 LSB

(20 kHz measurements are missing the correct bandpass filter and therefore show a larger THD that is coming from the generator)

	M2p.592x, DN2.592-xx, DN6.592-xx							
Test - sampling rate	20 MS/s							
Input Range	±200 mV		±500 mV		±1 V		±2 V	
Test Signal Frequency	1 MHz	n.a.	1 MHz	n.a.	1 MHz	n.a.	1 MHz	n.a.
SNR (typ)	≥ 77.2 dB	n.a.	≥ 79.8 dB	n.a.	≥ 81.0 dB	n.a.	≥ 75.0 dB	n.a.
THD (typ)	≤ 92.5 dB	n.a.	≤ -92.8 dB	n.a.	≤ -89.5 dB	n.a.	≤ -76.5 dB	n.a.
SFDR (typ), excl. harm.	≥ 103.0 dB	n.a.	≥ 103.0 dB	n.a.	≥ 105.0 dB	n.a.	≥ 93.0 dB	n.a.
ENOB based on SNR (typ)	≥ 12.5 LSB	n.a.	≥ 13.0 LSB	n.a.	≥ 13.2 LSB	n.a.	≥ 12.2 LSB	n.a.
ENOB based on SINAD (typ)	≥ 12.5 LSB	n.a.	≥ 13.0 LSB	n.a.	≥ 13.1 LSB	n.a.	≥ 11.8 LSB	n.a.

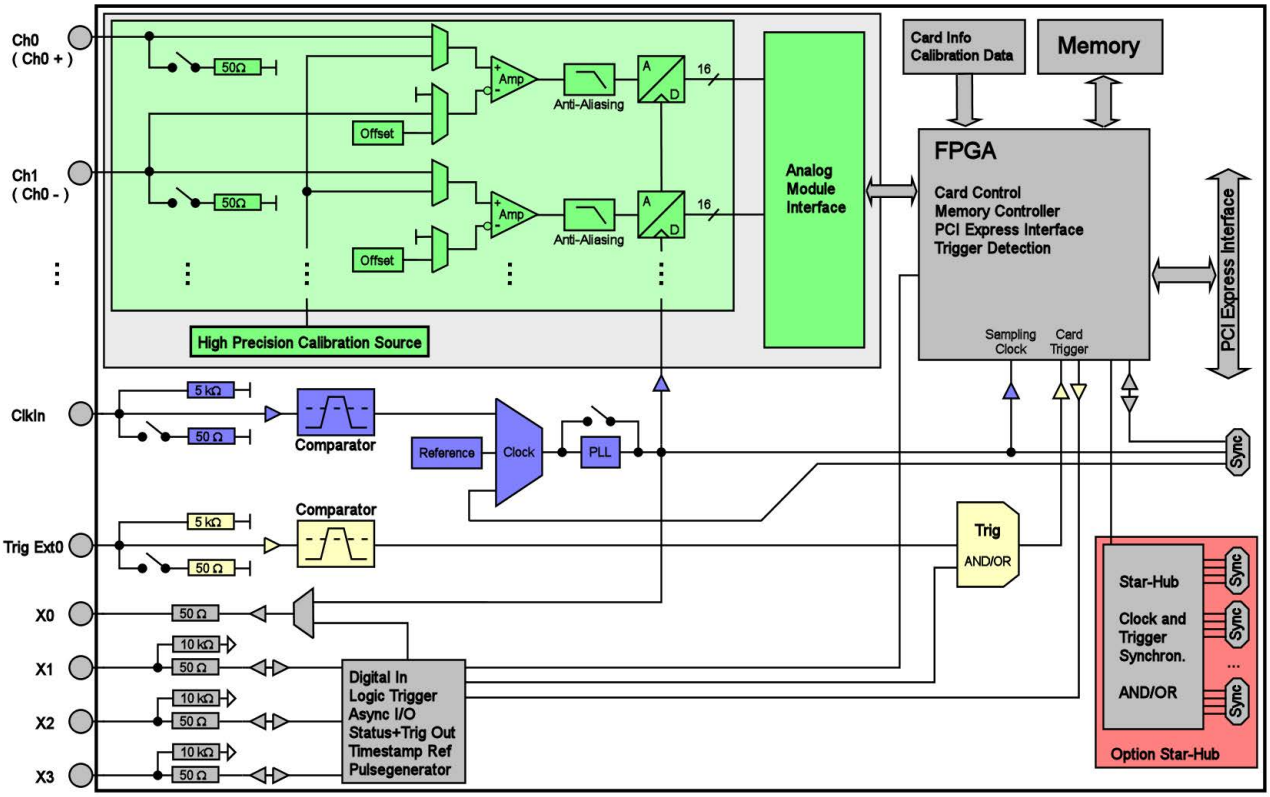
	M2p.593x, DN2.593-xx, DN6.593-xx, DN2.803-xx, DN2.813-xx							
Test - sampling rate	40 MS/s							
Input Range	±200 mV		±500 mV		±1 V		±2 V	
Test Signal Frequency	1 MHz	10 MHz	1 MHz	10 MHz	1 MHz	10 MHz	1 MHz	10 MHz
SNR (typ)	≥ 73.0 dB	≥ 72.6 dB	≥ 74.6 dB	≥ 74.4 dB	≥ 75.3 dB	≥ 75.3 dB	≥ 71.9 dB	≥ 71.8 dB
THD (typ)	≤ -87.8 dB	≤ -67.0 dB	≤ -89.0 dB	≤ -67.0 dB	≤ -86.1 dB	≤ -67.2 dB	≤ -79.0 dB	≤ -67.2 dB
SFDR (typ), excl. harm.	≥ 98.3 dB	≥ 96.5 dB	≥ 98.8 dB	≥ 99.5 dB	≥ 101.0 dB	≥ 100.0 dB	≥ 81.7 dB	≥ 91.3 dB
ENOB based on SNR (typ)	≥ 11.8 LSB	≥ 11.8 LSB	≥ 12.1 LSB	≥ 12.0 LSB	≥ 12.2 LSB	≥ 12.2 LSB	≥ 11.7 LSB	≥ 11.6 LSB
ENOB based on SINAD (typ)	≥ 11.8 LSB	≥ 10.7 LSB	≥ 12.1 LSB	≥ 10.7 LSB	≥ 12.2 LSB	≥ 10.8 LSB	≥ 11.6 LSB	≥ 10.7 LSB

	M2p.594x							
Test - sampling rate	80 MS/s							
Input Range	±200 mV		±500 mV		±1 V		±2 V	
Test Signal Frequency	1 MHz	10 MHz	1 MHz	10 MHz	1 MHz	10 MHz	1 MHz	10 MHz
SNR (typ)	≥ 70.6 dB	≥ 70.5 dB	≥ 72.9 dB	≥ 72.8 dB	≥ 74.2 dB	≥ 74.2 dB	≥ 69.8 dB	≥ 69.8 dB
THD (typ)	≤ -87.3 dB	≤ -76.9 dB	≤ -86.6 dB	≤ -76.3 dB	≤ -84.8 dB	≤ -70.1 dB	≤ -79.0 dB	≤ -77.9 dB
SFDR (typ), excl. harm.	≥ 97.5 dB	≥ 105.0 dB	≥ 101.0 dB	≥ 104.0 dB	≥ 100.0 dB	≥ 100.0 dB	≥ 96.9 dB	≥ 96.6 dB
ENOB based on SNR (typ)	≥ 11.4 LSB	≥ 11.4 LSB	≥ 11.8 LSB	≥ 11.8 LSB	≥ 12.0 LSB	≥ 12.0 LSB	≥ 11.2 LSB	≥ 11.2 LSB
ENOB based on SINAD (typ)	≥ 11.4 LSB	≥ 11.3 LSB	≥ 11.8 LSB	≥ 11.5 LSB	≥ 12.0 LSB	≥ 11.1 LSB	≥ 11.2 LSB	≥ 11.2 LSB

	M2p.596x, DN2.596-xx, DN6.596-xx, DN2.806-xx, DN2.816-xx											
Test - sampling rate	125 MS/s											
Input Range	±200 mV			±500 mV			±1 V			±2 V		
Test Signal Frequency	1 MHz	10 MHz	40 MHz	1 MHz	10 MHz	40 MHz	1 MHz	10 MHz	40 MHz	1 MHz	10 MHz	40 MHz
SNR (typ)	≥ 68.1 dB	≥ 66.2 dB	≥ 65.5 dB	≥ 70.5 dB	≥ 69.9 dB	≥ 68.7 dB	≥ 73.3 dB	≥ 72.7 dB	≥ 71.5 dB	≥ 67.8 dB	≥ 65.8 dB	≥ 65.1 dB
THD (typ)	≤ -81.5 dB	≤ -74.5 dB	≤ -53.7 dB	≤ -82.5 dB	≤ -77.6 dB	≤ -55.3 dB	≤ -83.3 dB	≤ -68.9 dB	≤ -57.3 dB	≤ -78.0 dB	≤ -75.6 dB	≤ -53.7 dB
SFDR (typ), excl. harm.	≥ 95.0 dB	≥ 93.4 dB	≥ 92.3 dB	≥ 97.5 dB	≥ 96.8 dB	≥ 94.0 dB	≥ 98.5 dB	≥ 98.1 dB	≥ 96.4 dB	≥ 91.5 dB	≥ 89.0 dB	≥ 89.0 dB
ENOB based on SNR (typ)	≥ 11.0 LSB	≥ 10.7 LSB	≥ 10.6 LSB	≥ 11.4 LSB	≥ 11.3 LSB	≥ 11.1 LSB	≥ 11.8 LSB	≥ 11.8 LSB	≥ 11.6 LSB	≥ 11.0 LSB	≥ 10.6 LSB	≥ 10.5 LSB
ENOB base SINAD (typ)	≥ 11.0 LSB	≥ 10.6 LSB	≥ 8.6 LSB	≥ 11.4 LSB	≥ 11.1 LSB	≥ 8.9 LSB	≥ 11.7 LSB	≥ 11.0 LSB	≥ 9.2 LSB	≥ 10.9 LSB	≥ 10.6 LSB	≥ 8.6 LSB

Dynamic parameters are measured at ±1 V input range (if no other range is stated) and 50 Ω termination with the samplerate specified in the table. Measured parameters are averaged 20 times to get typical values. Test signal is a pure sine wave generated by a signal generator and a matching bandpass filter. Amplitude is >99% of FSR. SNR and RMS noise parameters may differ depending on the quality of the used PC. SNR = Signal to Noise Ratio, THD = Total Harmonic Distortion, SFDR = Spurious Free Dynamic Range, SINAD = Signal Noise and Distortion, ENOB = Effective Number of Bits.

Hardware block diagram



Order Information

The card is delivered with 512 MiSample on-board memory and supports standard acquisition (Scope), FIFO acquisition (streaming), Multiple Recording, Gated Sampling, ABA mode and Timestamps. Operating system drivers for Windows/Linux 32 bit and 64 bit, examples for C/C++, LabVIEW (Windows), MATLAB (Windows and Linux), IVI, .NET, Delphi, Java, Python, Julia and a Base license of the oscilloscope software SBench 6 are included.

Adapter cables are not included. Please order separately!

PCI Express x4

Order no.	A/D Resolution	Standard mem	Single-Ended Inputs		Differential Inputs		
M2p.5911-x4	16 Bit	512 MiSample	2 channels	5 MS/s	2 channels	5 MS/s	
M2p.5912-x4	16 Bit	512 MiSample	4 channels	5 MS/s	2 channels	5 MS/s	
M2p.5916-x4	16 Bit	512 MiSample	4 channels	5 MS/s	4 channels	5 MS/s	
M2p.5913-x4	16 Bit	512 MiSample	8 channels	5 MS/s	4 channels	5 MS/s	
M2p.5920-x4	16 Bit	512 MiSample	1 channel	20 MS/s	1 channel	20 MS/s	OEM only
M2p.5921-x4	16 Bit	512 MiSample	2 channels	20 MS/s	2 channels	20 MS/s	
M2p.5922-x4	16 Bit	512 MiSample	4 channels	20 MS/s	2 channels	20 MS/s	
M2p.5926-x4	16 Bit	512 MiSample	4 channels	20 MS/s	4 channels	20 MS/s	
M2p.5923-x4	16 Bit	512 MiSample	8 channels	20 MS/s	4 channels	20 MS/s	
M2p.5930-x4	16 Bit	512 MiSample	1 channel	40 MS/s	1 channel	40 MS/s	OEM only
M2p.5931-x4	16 Bit	512 MiSample	2 channels	40 MS/s	2 channels	40 MS/s	
M2p.5932-x4	16 Bit	512 MiSample	4 channels	40 MS/s	2 channels	40 MS/s	
M2p.5936-x4	16 Bit	512 MiSample	4 channels	40 MS/s	4 channels	40 MS/s	
M2p.5933-x4	16 Bit	512 MiSample	8 channels	40 MS/s	4 channels	40 MS/s	
M2p.5940-x4	16 Bit	512 MiSample	1 channel	80 MS/s	1 channel	80 MS/s	
M2p.5941-x4	16 Bit	512 MiSample	2 channels	80 MS/s	2 channels	80 MS/s	
M2p.5942-x4	16 Bit	512 MiSample	4 channels	80 MS/s	2 channels	80 MS/s	
M2p.5946-x4	16 Bit	512 MiSample	4 channels	80 MS/s	4 channels	80 MS/s	
M2p.5943-x4	16 Bit	512 MiSample	8 channels	80 MS/s	4 channels	80 MS/s	
M2p.5960-x4	16 Bit	512 MiSample	1 channel	125 MS/s	1 channel	125 MS/s	
M2p.5961-x4	16 Bit	512 MiSample	2 channels	125 MS/s	2 channels	125 MS/s	
M2p.5962-x4	16 Bit	512 MiSample	4 channels	125 MS/s	2 channels	125 MS/s	
M2p.5966-x4	16 Bit	512 MiSample	4 channels	125 MS/s	4 channels	125 MS/s	
M2p.5968-x4	16 Bit	512 MiSample	4 channels 8 channels	125 MS/s 80 MS/s	4 channels	125 MS/s	

Options

Order no.	Option
M2p.xxxx-SH6ex [1]	Synchronization Star-Hub for up to 6 cards incl. cables, only one slot width, card length 245 mm
M2p.xxxx-SH6tm [1]	Synchronization Star-Hub for up to 6 cards incl. cables, two slots width, standard card length
M2p.xxxx-SH16ex [1]	Synchronization Star-Hub for up to 16 cards incl. cables, only one slot width, card length 245 mm
M2p.xxxx-SH16tm [1]	Synchronization Star-Hub for up to 16 cards incl. cables, two slots width, standard card length
M2p.xxxx-DigFX2	16 additional multi-purpose I/O lines on separate slot bracket, FX2 connector (incl. Cab-d40-idc-100)
M2p.xxxx-DigSMB	16 additional multi-purpose I/O lines, 10 on separate slot bracket, 6 internal connectors
M2p-upgrade	Upgrade for M2p.xxxx: Later installation of options Star-Hub or Dig.

Firmware Options

Order no.	Option
M2p.xxxx-PulseGen	Firmware Option: adds 4 freely programmable digital pulse generators that use the XIO lines X0 to X3 for output (later installation by firmware upgrade available)
M2p.xxxx-spavg	Signal Processing Firmware Option: Block Average (later firmware-upgrade available)

Services

Order no.	
Recal	Recalibration at Spectrum incl. calibration protocol
Card-Prot-17025	ISO 17025 Calibration Certificate for one card; add-on for new cards or for recalibration

Cables

		Order no.					
for Connections	Length	to BNC male	to BNC female	to SMA male	to SMA female	to SMB female	
Analog/Clock/Trig/Dig	80 cm	Cab-3f-9m-80	Cab-3f-9f-80	Cab-3f-3mA-80	Cab-3f-3fA-80	Cab-3f-3f-80	
Analog/Clock/Trig/Dig	200 cm	Cab-3f-9m-200	Cab-3f-9f-200	Cab-3f-3mA-200	Cab-3f-3fA-200	Cab-3f-3f-200	
Probes (short)	5 cm		Cab-3f-9f-5				
Clk-Out/Trig-Out/Extra	80 cm	Cab-1m-9m-80	Cab-1m-9f-80	Cab-1m-3mA-80	Cab-1m-3fA-80	Cab-1m-3f-80	
Clk-Out/Trig-Out/Extra	200 cm	Cab-1m-9m-200	Cab-1m-9f200	Cab-1m-3mA-200	Cab-1m-3fA-200	Cab-1m-3f-200	
Information	The standard adapter cables are based on RG174 cables and have a nominal attenuation of 0.3 dB/m at 100 MHz.						

		to 2x20 pole IDC	to 40 pole FX2			
M2p.xxxx-DigFX2	100 cm	Cab-d40-idc-100	Cab-d40-d40-100			

Amplifiers

Order no.	Bandwidth	Connection	Input Impedance	Coupling	Amplification
SPA.1412 [2]	200 MHz	BNC	1 MOhm	AC/DC	x10/x100 (20/40 dB)
SPA.1411 [2]	200 MHz	BNC	50 Ohm	AC/DC	x10/x100 (20/40 dB)
SPA.1232 [2]	10 MHz	BNC	1 MOhm	AC/DC	x100/x1000 (40/60 dB)
SPA.1231 [2]	10 MHz	BNC	50 Ohm	AC/DC	x100/x1000 (40/60 dB)
Information	External Amplifiers with one channel, BNC/SMA female connections on input and output, manually adjustable offset, manually switchable settings. An external power supply for 100 to 240 VAC is included. Please be sure to order an adapter cable matching the amplifier connector type and matching the connector type for your A/D card input.				

Software SBench6

Order no.	
SBench6	Base version included in delivery. Supports standard mode for one card.
SBench6-Pro	Professional version for one card: FIFO mode, export/import, calculation functions, ...

Software SBench6

Order no.	
SBench6-Multi	Option multiple cards: Needs SBench6-Pro. Handles multiple synchronized cards in one system.
Volume Licenses	Please ask Spectrum for details.

Software Options

Order no.	
SPc-RServer	Remote Server Software Package - LAN remote access for M2i/M3i/M4i/M4x/M2p/M5i cards
SPc-SCAPP	Spectrum's CUDA Access for Parallel Processing - SDK for direct data transfer between Spectrum card and CUDA GPU. Includes RDMA activation and examples. Only working for Data Acquisition or AWG mode, not usable for DDS mode

Technical changes and printing errors possible

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