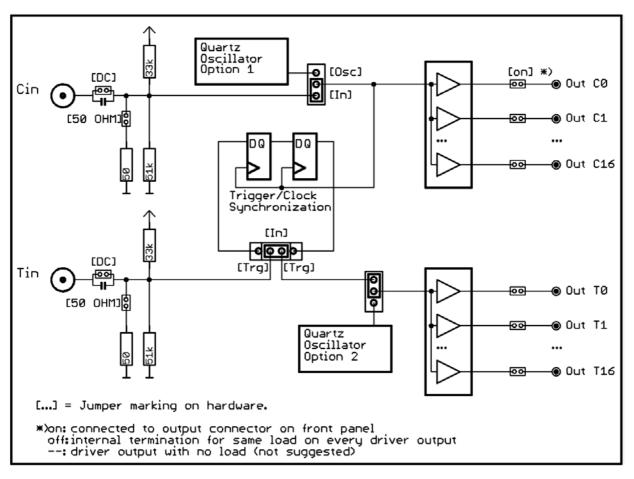


M2i.ClkDist / M2i.ClkDist-exp - Clock and Trigger Distribution Card

- Easy synchronization of distributed systems
- 2 independent 1 onto 17 distribution channels for 2 x clock or clock and trigger
- Very low channel-to-channel skew
- Passive PCI or PCI Express card, no programming necessary
- LVTTL outputs, capable of driving 50 ohm loads
- Outputs can be deactivated by jumper
- As an option: useable as clock source equipped with separetely orderd quartz oscillator



Hardware block diagram



Technical Data

Inputs

 $\begin{array}{ll} \mbox{Input voltage low level} & \geq 0 \ \mbox{V and} \leq 0.8 \ \mbox{V (min level} \cdot 0.3 \ \mbox{V)} \\ \mbox{Input voltage high level} & \geq 2.0 \ \mbox{V and} \leq 3.3 \ \mbox{V (max level} \ 3.8 \ \mbox{V)} \\ \mbox{Trigger pulse width on sync feature use} & \geq 2 \ \mbox{samplings clocks} \\ \end{array}$

Termination 20 kOhm, selectable per jumper to 50 ohm

Coupling DC, selectable by jumper to AC Max input clock rate 167 MHz

Max trigger synchronization rate

Connector type

Max trigger synchronization rate

125 MHz

3mm SMB male

Power consumption	PCI/PCI-X	PCI/PCI-X PCIe	
	3,3 V	3,3V	Total
Idle Running, no loads	0.7 A	0.7 A	2.3 W
4 output channels 50 ohm load*	0.9 A	0.9 A	3.0 W
17 output channels 50 ohm load*	1.2 A	1.2 A	4.0 W
34 output channels 50 ohm load*	1 6 Δ	1 6 Δ	5 3 W/

^{*}measured with 125 MHz clock signal, 50% duty cycle

Outputs

Output voltage low level (50 ohm load) $\geq 0.5 \text{ V}$ Output voltage high level (50 ohm load) $\geq 2.4 \text{ V}$ and $\leq 0.5 \text{ V}$ max channel to channel skew (one bank) 150 ps 275 ps Output impedance (typical) 7 ohm Max propagation delay 4.3 ns

Outputs in bank 0 0, 1, 2, 3, 8, 9, 10, 11 Outputs in bank 1 4, 5, 6, 7, 13, 14, 15, 16

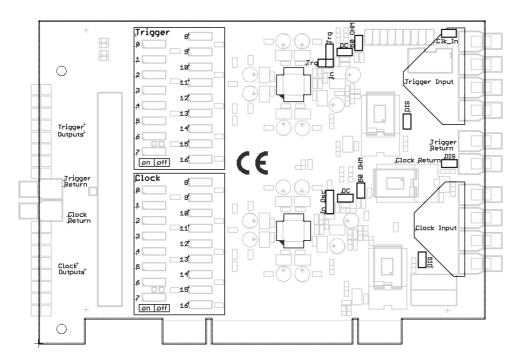
Outputs in bank 2

Connector type MMCX female

Environmental and Physical details

Dimension (PCB only) 174 mm x 107 mm (half PCI length)

Jumper Placement



Order Information

PCI/PCI-X

	Order no.	Description
Ī		Clock and trigger distribution card with 2 times 1 into 17 channels distribution, short PCI 32 bit card, no cables included
	M2i.ClkDist-exp	Clock and trigger distribution card with 2 times 1 into 17 channels distribution, short PCI Express x1 card, no cables included
		Quartz option with xx MHz and a accuracy of yy ppm (e.g. 40M20 = 40 MHz 20 ppm), please request an individual quote.

Cables

Order no.	Option
Cab-1 m-9 m-80	Adapter cable MMCX male to BNC male, 80 cm
Cab-1 m-9f-80	Adapter cable MMCX male to BNC female, 80 cm
Cab-1 m-9 m-200	Adapter cable MMCX male to BNC male, 200 cm
Cab-1 m-9f-200	Adapter cable MMCX male to BNC female, 200 cm
Cab-1 m-3f-80	Adapter cable MMCX male to SMB female, 80 cm
Cab-1 m-3f-200	Adapter cable MMCX male to SMB female, 200 cm
Cab-3f-9m-80	Adapter cable SMB female to BNC male, 80 cm
Cab-3f-9f-80	Adapter cable SMB female to BNC female, 80 cm
Cab-3f-3f-80	Adapter cable SMB female to SMB female, 80 cm
Cab-3f-9m-200	Adapter cable SMB female to BNC male, 200 cm

Cables

Order no.	Option
Cab-3f-9f-200	Adapter cable SMB female to BNC female, 200 cm
Information	The standard adapter cables are based on RG174 cables and have a nominal attenuation of 0.3 dB/m at 100 MHz.

Technical changes and printing errors possible

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