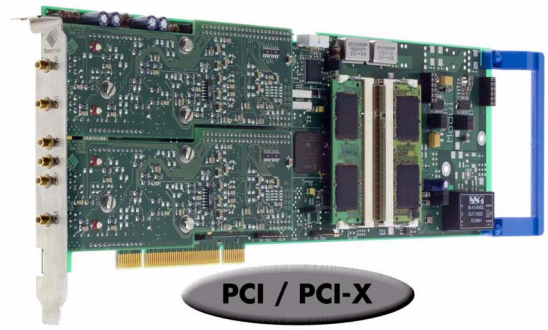
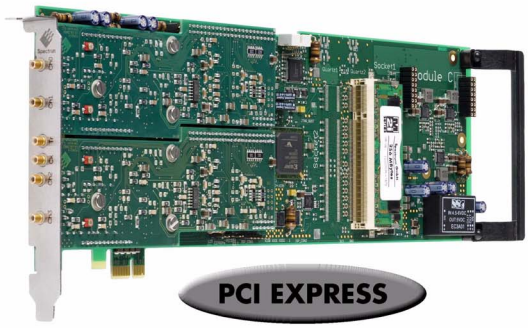
M2i.40xx - 14 bit transient recorder up to 50 MS/s

- **Versions with 20 MS/s and 50 MS/s available**
- **1, 2 or 4 channels acquisition**
- **Simultaneously sampling on all channels**
- **Separate ADC and amplifier per channel**
- **6 input ranges: ±200 mV up to ± 10 V**
- **Up to 1 GSample (2 GByte) on-board memory**
- **256 MSample standard memory installed**
- **Window, pulse width, re-arm, OR/AND trigger**
- **Programmable input offset of ±200%**
- **Synchronization of up to 16 cards per system and up to 271 cards with system sync**
- **Synchronous digital channels as option**





- 66 MHz 32 bit PCI-X interface
- 5V / 3.3V PCI compatible
- 100% compatible to conventional PCI > V2.1
- Sustained streaming mode up to 245 MB/s

- 2,5 GBit x1 PCIe Interface
- Works with x1/x4/x8/x16* PCIe slots
- Software compatible to PCI
- Sustained streaming mode up to 160 MB/s

Operating Systems	**Recommended Software**	**Drivers**
• Windows 7 (SP1), 8, 10, Server 2008 R2 and newer • Linux Kernel 2.6, 3.x, 4.x, 5.x • Windows/Linux 32 and 64 bit	• Visual C++, Delphi, C++ Builder, GNU C++, VB.NET, C#, J#, Java, Python • SBench 6	• MATLAB • LabVIEW • IVI

Model	1 channel	2 channels	4 channels
M2i.4020	20 MS/s		
M2i.4021	20 MS/s	20 MS/s	
M2i.4022	20 MS/s	20 MS/s	20 MS/s
M2i.4030	50 MS/s		
M2i.4031	50 MS/s	50 MS/s	
M2i.4032	50 MS/s	50 MS/s	50 MS/s
Modell	**A/D channel**	**Digital**	
M2i.4028	20 MS/s	20 MS/s	
M2i.4038	50 MS/s	50 MS/s	

Please see separate data sheet for special version 4028 and 4038.

General Information

The M2i.40xx series is best suitable for applications that need high sampling rates as well as a maximum signal dynamic. These boards offer a resolution four times higher than 12 bit boards. On the M2i.40xx every channel has its own amplifier and A/D converter. Each input channel can be adapted to a wide variety of signal sources. This is done by software selecting a matching input range, an input impedance and an individual input offset compensation. The user will easily find a matching solution from the six offered models. These versions are working with sampling rates of 20 MS/s or 50 MS/s and have one, two or four channels. They can also be updated to a multi-channel system using the synchronization option. Data is written in the internal 128 MSample up to 1 GSample large memory. All boards of the M2i.40xx series may use the whole installed on-board memory completely for the currently activated number of channels. This memory can also be used as a FIFO buffer. In FIFO mode data can be transferred on-line directly into the PC RAM or to hard disk.

*Some x16 PCIe slots are for the use of graphic cards only and can not be used for other cards.

Software Support

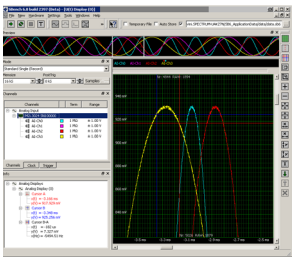
Windows drivers

The cards are delivered with drivers for Windows 7, Windows 8 and Windows 10 (32 bit and 64 bit). Programming examples for Visual C++, C++ Builder, Delphi, Visual Basic, VB.NET, C#, J#, Python, Java and IVI are included.

Linux Drivers

All cards are delivered with full Linux support. Pre compiled kernel modules are included for the most common distributions like Fedora, Suse, Ubuntu LTS or Debian. The Linux support includes SMP systems, 32 bit and 64 bit systems, versatile programming examples for GNU C++, Python as well as the possibility to get the driver sources for your own compilation.

SBench 6



A base license of SBench 6, the easy-to-use graphical operating software for Spectrum cards, is included in the delivery. The base license makes it is possible to test the card, display acquired data and make some basic measurements. It's a valuable tool for checking the card's performance and assisting with the unit's initial setup. The cards also come with a demo license for the SBench 6 professional version. This license gives the user the opportunity to test the additional features of the professional version with their hardware. The professional version contains several advanced measurement functions, such as FFTs and X/Y display, import and export utilities as well as support for all acquisition modes including data streaming. Data streaming allows the cards to continuously acquire data and transfer it directly to the PC RAM or hard disk. SBench 6 has been optimized to handle data files of several GBytes. SBench 6 runs under Windows as well as Linux (KDE, GNOME and Unity) operating systems. A test version of SBench 6 can be downloaded directly over the internet and can run the professional version in a simulation mode without any hardware installed. Existing customers can also request a demo license for the professional version from Spectrum. More details on SBench 6 can be found in the SBench 6 data sheet.

Third-party products

Spectrum supports the most popular third-party software products such as LabVIEW, MATLAB or LabWindows/CVI. All drivers come with detailed documentation and working examples are included in the delivery. Support for other software packages, like VEE or DasyLab, can also be provided on request.

Hardware features and options

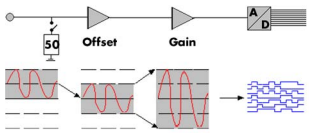
PCI/PCI-X

The cards with PCI/PCI-X bus connector use 32 Bit and up to 66 MHz clock rate for data transfer. They are 100% compatible to Conventional PCI > V2.1. The universal interface allows the use in PCI slots with 5 V I/O and 3.3 V I/O voltages as well as in PCI-X or PCI 64 slots. The maximum sustained data transfer rate is 245 MByte/s per bus segment.

PCI Express

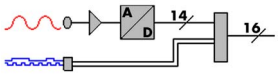
The cards with PCI Express use a x1 PCIe connector. They can be used in PCI Express x1/x4/x8/x16 slots, except special graphic card slots, and are 100% software compatible to Conventional PCI > V2.1. The maximum sustained data transfer rate is 160 MByte/s per slot.

Input Amplifier



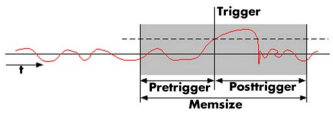
The analog inputs can be adapted to real world signals using a wide variety of settings that are individual for each channel. By using software commands the input termination can be changed between 50 Ohm and 1 MOhm, one can select a matching input range and the signal offset can be compensated for.

Digital inputs



This option acquires additional synchronous digital channels phase-stable with the analog data. When the option is installed there are 2 additional digital inputs for every analog A/D channel.

Ring buffer mode



The ring buffer mode is the standard mode of all oscilloscope instruments. Digitized data is continuously written into a ring memory until a trigger event is detected. After the trigger, post-trigger samples are recorded and pre-trigger samples can also be stored. The number of pre-trigger samples available simply equals the total ring memory size minus the number of post trigger samples.

FIFO mode

The FIFO mode is designed for continuous data transfer between measurement board and PC memory (up to 245 MB/s on a PCI-X slot, up to 125 MB/s on a PCI slot and up to 160 MB/s on a PCIe slot) or hard disk. The control of the data stream is done automatically by the driver on interrupt request. The complete installed on-board memory is used for buffer data, making the continuous streaming extremely reliable.

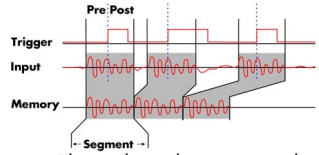
Channel trigger

The data acquisition instruments offer a wide variety of trigger modes. Besides the standard signal checking for level and edge as known from oscilloscopes it's also possible to define a window trigger. All trigger modes can be combined with the pulsewidth trigger. This makes it possible to trigger on signal errors like too long or too short pulses. In addition to this a re-arming mode (for accurate trigger recognition on noisy signals) the AND/OR conjunction of different trigger events is possible. As a unique feature it is possible to use deactivated channels as trigger sources.

External trigger I/O

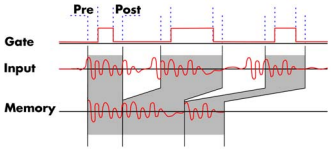
All instruments can be triggered using an external TTL signal. It's possible to use positive or negative edge also in combination with a programmable pulse width. An internally recognised trigger event can - when activated by software - be routed to the trigger connector to start external instruments.

Multiple Recording



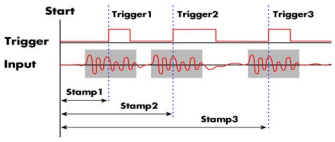
The Multiple Recording mode allows the recording of several trigger events with an extremely short re-arming time. The hardware doesn't need to be restarted in between. The on-board memory is divided in several segments of the same size. Each of them is filled with data if a trigger event occurs. Pre- and posttrigger of the segments can be programmed. The number of acquired segments is only limited by the used memory and is unlimited when using FIFO mode.

Gated Sampling



The Gated Sampling mode allows data recording controlled by an external gate signal. Data is only recorded if the gate signal has a programmed level. In addition a pre-area before start of the gate signal as well as a post area after end of the gate signal can be acquired. The number of gate segments is only limited by the used memory and is unlimited when using FIFO mode.

Timestamp

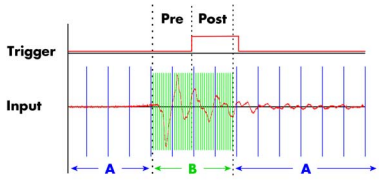


The timestamp function writes the time positions of the trigger events in an extra memory. The timestamps are relative to the start of recording, a defined zero time, externally synchronized to a radio clock, an IRIG-B a GPS receiver. Using the external synchronization gives a precise time relation for acquisitions of systems on different locations.

External clock I/O

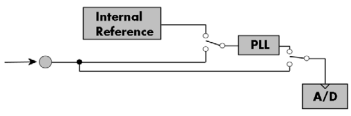
Using a dedicated connector a sampling clock can be fed in from an external system. It's also possible to output the internally used sampling clock to synchronise external equipment to this clock.

ABA mode



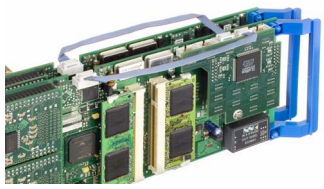
The ABA mode combines slow continuous data recording with fast acquisition on trigger events. The ABA mode works like a slow data logger combined with a fast digitizer. The exact position of the trigger events is stored as timestamps in an extra memory.

Reference clock



The option to use a precise external reference clock (typically 10 MHz) is necessary to synchronize the instrument for high-quality measurements with external equipment (like a signal source). It's also possible to enhance the stability of the sampling clock in this way. The driver automatically generates the requested sampling clock from the fed in reference clock.

Star-Hub

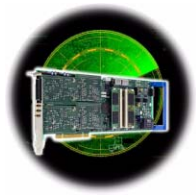


The star-hub is an additional module allowing the phase stable synchronisation of up to 16 boards in one system. Independent of the number of boards there is no phase delay between all channels. The star-hub distributes trigger and clock information between all boards. As a result all connected boards are running with the same clock and the same trigger. All trigger sources can be combined with OR/AND allowing all channels of all cards to be trigger source at the same time. The star-hub is available as 5 card and 16 card version. The 5 card version doesn't need an extra slot.

BaseXIO (Asynchronous I/O, enhanced trigger)

The BaseXIO option offers 8 asynchronous digital I/O lines on the base card. The direction can be selected by software in groups of four. This allows e.g. external equipment control or status monitoring. Two of these lines can also be used as additional external trigger sources. This allows the building of complex trigger conjunctions with external gated triggers as well as AND/OR conjunction of multiple external trigger sources like, for example, the picture and row synchronisation of video signals. In addition one of the I/O lines can be used as reference clock for the Timestamp counter.

40x8 RADAR optimized special version

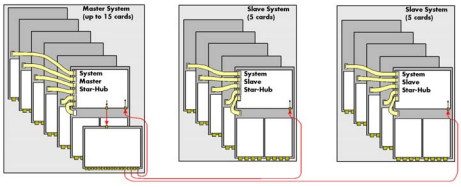


Both of the special cards 4028 and 4038 combine analog and digital data acquisition to have a synchronous recording of both a radar echo and the current angle position. The digital azimuth and elevation angle data can be acquired in parallel or using encoder inputs. Data is either stored continuously and synchronously with the analog data or as a marker with each segment of analog data. Detailed information on these cards is available with a separate data sheet.

External Amplifiers

For the acquisition of extremely small voltage levels with a high bandwidth a series of external amplifiers is available. Each of the one channel amplifiers is working with a fixed input impedance and allows - depending on the bandwidth - to select different amplification levels between x10 (20 dB) up to x1000 (60 dB). Using the external amplifiers of the SPA series voltage levels in the uV and mV area can be acquired.

271 synchronous cards with theSystem Star-Hub



With the help of multiple system star-hubs it is possible to link up to 17 system phase synchronous with each other. Each system can then contain up to 16 cards (master only 15). In total 271 cards can be used fully synchronously in a bunch of systems. One master system distributes clock and trigger signal to all connected slave systems.

Technical Data

Analog Inputs

Resolution		14 bit
Input Range	software programmable	±200 mV, ±500 mV, ±1 V, ±2 V, ±5 V, ±10 V
Input Mode	fixed	bipolar, single-ended
Input Offset	software programmable	±200% of input range in steps of 1%
ADC Differential non linearity (DNL)	ADC only	±0.5 LSB
ADC Integral non linearity (INL)	ADC only	±1 LSB
Offset error (full speed)	after warm-up and calibration	≤ 0.1% of range
Gain error (full speed)	after warm-up and calibration	≤ 1% of current value
Crosstalk: 1 MHz Signal, 50 Ω termination	all input ranges	≤ -80 dB on adjacent channels
Crosstalk: 1 MHz Signal, 1 MΩ termination	all input ranges	≤ -65 dB on adjacent channels
Analog Input impedance	software programmable	50 Ω or 1 MΩ || 25 pF
Analog input coupling	fixed	DC
Over voltage protection (active card)	ranges ≤ ±1 V	±5 V
Over voltage protection (active card)	ranges > ±1 V	±50 V
Input signal with 50 Ω termination		max 5 V rms
Channel selection	software programmable	1, 2 or 4 (maximum is model dependent)

Trigger

Available trigger modes	software programmable	Channel Trigger, External, Software, Window, Pulse, Re-Arm, Or/And, Delay
Trigger level resolution	software programmable	10 bit
Trigger edge	software programmable	Rising edge, falling edge or both edges
Trigger pulse width	software programmable	0 to [64k - 1] samples in steps of 1 sample
Trigger delay	software programmable	0 to [64k - 1] samples in steps of 1 sample
Multi, Gate: re-arming time		< 4 samples (+ programmed pretrigger)
Pretrigger at Multi, ABA, Gate, FIFO	software programmable	4 up to [8176 Samples / number of active channels] in steps of 4
Posttrigger	software programmable	4 up to [8G - 4] samples in steps of 4 (defining pretrigger in standard scope mode)
Memory depth	software programmable	8 up to [installed memory / number of active channels] samples in steps of 4
Multiple Recording/ABA segment size	software programmable	8 up to [installed memory / 2 / active channels] samples in steps of 4
Trigger output delay		One positive edge after internal trigger event
Internal/External trigger accuracy		1 sample
External trigger type (input and output)		3.3V LVTTL compatible (5V tolerant with base card hardware version > V20)
External trigger input		Low ≤ 0.8 V, High ≥ 2.0 V, ≥ 8 ns in pulse stretch mode, ≥ 2 clock periods all other modes
External trigger maximum voltage		-0.5 V up to +5.7 V (internally clamped to 5.0V, 100 mA max. clamping current)
Trigger impedance	software programmable	50 Ohm / high impedance (> 4kOhm)
External trigger output type		3.3 V LVTTL
External trigger output levels		Low ≤ 0.4 V, High ≥ 2.4 V, TTL compatible
External trigger output drive strength		Capable of driving 50 ohm load, maximum drive strength ±128 mA

Clock

Clock Modes	software programmable	internal PLL, internal quartz, external clock, external divided, external reference clock, sync
Internal clock range (PLL mode)	software programmable	1 kS/s to max using internal reference, 50kS/s to max using external reference clock
Internal clock accuracy		≤ 20 ppm
Internal clock setup granularity		≤1% of range (100M, 10M, 1M, 100k,...): Examples: range 1M to 10M: stepsize ≤ 100k
External reference clock range	software programmable	≥ 1.0 MHz and ≤ 125.0 MHz
External clock impedance	software programmable	50 Ohm / high impedance (> 4kOhm)
External clock range		see „Dynamic Parameters" table below
External clock delay to internal clock		5.4 ns
External clock type/edge		3.3V LVTTL compatible, rising edge used
External clock input		Low level ≤ 0.8 V, High level ≥ 2.0 V, duty cycle: 45% - 55%
External clock maximum voltage		-0.5 V up to +3.8 V (internally clamped to 3.3V, 100 mA max. clamping current) (not 5V tolerant)
External clock output type		3.3 V LVTTL
External clock output levels		Low ≤ 0.4 V, High ≥ 2.4 V, TTL compatible
External clock output drive strength		Capable of driving 50 ohm load, maximum drive strength ±128 mA
Synchronization clock divider	software programmable	2 up to [8k - 2] in steps of 2
ABA mode clock divider for slow clock	software programmable	8 up to 524280 in steps of 8

BaseXIO Option

BaseXIO modes	software programmable	Asynch digital I/O, 2 additional trigger, timestamp reference clock, timestamp digital inputs
BaseXIO direction	software programmable	Each 4 lines can be programmed in direction
BaseXIO input		TTL compatible: Low ≤ 0.8 V, High ≥ 2.0 V
BaseXIO input impedance		4.7 kOhm towards 3.3 V
BaseXIO input maximum voltage		-0.5 V up to +5.5 V
BaseXIO output type		3.3 V LVTLL
BaseXIO output levels		TTL compatible: Low ≤ 0.4 V, High ≥ 2.4 V
BaseXIO output drive strength		32 mA maximum current, no 50 Ω loads

Digital Inputs Option

Digital data acquisition modes	software programmable	2 digital inputs per active analog channel
Digital inputs delay to analog sample		-7 Samples
Input Impedance		110 Ω at 2.5 V
Maximum voltage		-0.3 V up to +5.5 V (internally clamped to 3.3V and ground, 200 mA max. clamping current)
Input voltage		Low ≤ 0.8 V, High ≥ 2.0 V (TTL compatible)

Connectors

Analog Inputs		3 mm SMB male (one for each single-ended input)	Cable-Type: Cab-3f-xx-xx
Trigger Input/Output	programmable direction	3 mm SMB male (one connector)	Cable-Type: Cab-3f-xx-xx
Clock Input/Output	programmable direction	3 mm SMB male (one connector)	Cable-Type: Cab-3f-xx-xx
Option Digital Inputs/Outputs		40 pole half pitch (Hirose FX2 series)	Cable-Type: Cab-d40-xx-xx
Option BaseXIO		8 x 3 mm SMB male on extra bracket, internally 8 x MMCX female	

Environmental and Physical Details

Dimension (PCB only)	312 mm x 107 mm (full PCI length)
Width (Standard or with option star-hub 5)	1 full size slot
Width (star-hub 16)	additionally back of adjacent neighbour slots
Width (with option BaseXIO)	additionally extra bracket on neighbour slot
Width (with option -digin, -digout or -60xx-AmpMod)	additionally half length of adjacent neighbour slot
Weight (depending on version)	290g (smallest version) up to 460g (biggest version with all options, including star-hub)
Warm up time	10 minutes
Operating temperature	0°C to 50°C
Storage temperature	-10°C to 70°C
Humidity	10% to 90%

PCI/PCI-X specific details

PCI / PCI-X bus slot type	32 bit 33 MHz or 32 bit 66 MHz
PCI / PCI-X bus slot compatibility	32/64 bit, 33-133 MHz, 3,3 V and 5 V I/O
Sustained streaming mode	> 245 MB/s (in a PCI-X slot clocked at 66 MHz or higher)

PCI Express specific details

PCIe slot type	x1 Generation 1
PCIe slot compatibility (physical)	x1, x4, x8, x16
PCIe slot compatibility (electrical)	x1, x2, x4, x8, x16 with Generation 1, Generation 2, Generation 3, Generation 4
Sustained streaming mode	> 160 MB/s

Certification, Compliance, Warranty

EMC Immunity	Compliant with CE Mark
EMC Emission	Compliant with CE Mark
Product warranty	5 years starting with the day of delivery
Software and firmware updates	Life-time, free of charge

Power Consumption

		PCI / PCI-X			PCI EXPRESS		
		3.3 V	**5 V**	**Total**	**3.3V**	**12V**	**Total**
M2i.40x0 (256 MSample memory)		2.0 A	0.6 A	9.6 W	0.4 A	1.1 A	14.5 W
M2i.40x1 (256 MSample memory)		2.2 A	0.8 A	11.3 W	0.4 A	1.2 A	15.7 W
M2i.40x2 (256 MSample memory)		2.5 A	1.6 A	16.3 W	0.4 A	1.6 A	20.5 W
M2i.4032 (2 GSample memory)	max power	3.6 A	1.6 A	19.9 W	0.4 A	2.2 A	27.7 W
M2i.40x8 (256 MSample memory)		2.4 A	1.2 A	13.9 W	0.4 A	1.4 A	18.1 W

MTBF

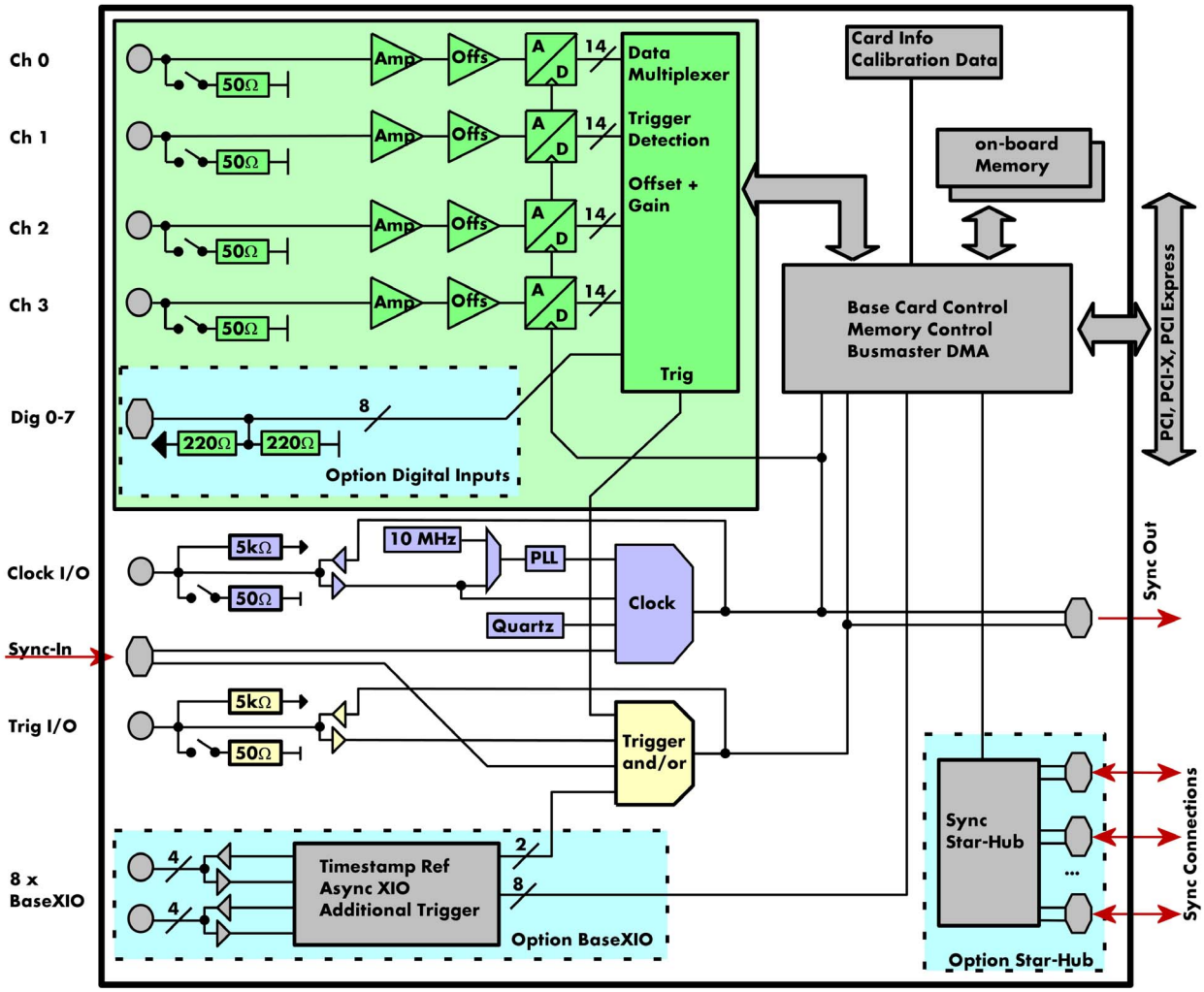
MTBF	400000 hours

Dynamic Parameters

	M2i.4020 M2i.4021 M2i4028		M2i.4022		M2i.4030 M2i.4031 M2i.4038		M2i.4032	
max internal clock	20 MS/s		20 MS/s		50 MS/s		50 MS/s	
min internal clock	1kS/s		1kS/s		1kS/s		1kS/s	
max external clock	20 MS/s		20 MS/s		50 MS/s		50 MS/s	
min external clock	500 kS/s		500 kS/s		500 kS/s		500 kS/s	
-3 dB bandwidth	DC to 10 MHz		DC to 10 MHz		DC to 25 MHz		DC to 25 MHz	
Zero noise level at 50 Ohm	≤ 2.1 LSB rms		≤ 2.6 LSB rms		≤ 2.9 LSB rms		≤ 3.6 LSB rms	
Test - sampling rate	20 MS/s		20 MS/s		50 MS/s		50 MS/s	
Test signal frequency	1 MHz	4 MHz	1 MHz	4 MHz	1 MHz	4 MHz	1 MHz	4 MHz
SNR (typ.)	71.5 dB	67.2 dB	71.4 dB	67.0 dB	70.5 dB	68.5 dB	69.0 dB	66.0 dB
THD (typ.)	-74.5 dB	-66.5 dB	-74.5 dB	-65.5 dB	-73.0 dB	-63.2 dB	-72.0 dB	-62.5 dB
SFDR (typ.), excl. harm.	88.0 dB	74.2 dB	86.3 dB	74.0 dB	87.2 dB	81.5 dB	85.0 dB	80.3 dB
ENOB (based on SNR	11.5 bit	10.8 bit	11.5 bit	10.8 bit	11.4 bit	11.0 bit	11.1 bit	10.6 bit
ENOB (based on SINAD)	11.2 bit	10.3 bit	11.2 bit	10.2 bit	11.1 bit	10.0 bit	10.8 bit	9.8 bit

Dynamic parameters are measured at ±1 V input range (if no other range is stated) and 50Ω termination with the samplerate specified in the table. Measured parameters are averaged 20 times to get typical values. Test signal is a pure sine wave generated by a signal generator and a matching bandpass filter. Amplitude is >99% of FSR. SNR and RMS noise parameters may differ depending on the quality of the used PC. SNR = Signal to Noise Ratio, THD = Total Harmonic Distortion, SFDR = Spurious Free Dynamic Range, SINAD = Signal Noise and Distortion, ENOB = Effective Number of Bits.

Hardware block diagram



Order Information

The card is delivered with 256 MSample on-board memory and supports standard acquisition (Scope), FIFO acquisition (streaming), Multiple Recording, Gated Sampling, ABA mode and Timestamps. Operating system drivers for Windows/Linux 32 bit and 64 bit, examples for C/C++, LabVIEW (Windows), MATLAB (Windows and Linux), IVI, .NET, Delphi, Java, Python and a Base license of the oscilloscope software SBench 6 are included. Drivers for other 3rd party products like VEE or DASYLab may be available on request.

Adapter cables are not included. Please order separately!

PCI Express (PCIe) PCI/PCI-X

PCI Express	PCI/PCI-X	Standard mem	1 channel	2 channels	4 channels
M2i.4020-exp	M2i.4020	256 MSample	20 MS/s		
M2i.4021-exp	M2i.4021	256 MSample	20 MS/s	20 MS/s	
M2i.4022-exp	M2i.4022	256 MSample	20 MS/s	20 MS/s	20 MS/s
M2i.4028-exp	M2i.4028	256 MSample	20 MS/s (1 channel analog + 16 bit digital)		
M2i.4030-exp	M2i.4030	256 MSample	50 MS/s		
M2i.4031-exp	M2i.4031	256 MSample	50 MS/s	50 MS/s	
M2i.4032-exp	M2i.4032	256 MSample	50 MS/s	50 MS/s	50 MS/s
M2i.4038-exp	M2i.4038	256 MSample	50 MS/s (1 channel analog + 16 bit digital)		

Memory

Order no.	Option
M2i.xxxx-512MS	Memory upgrade to 512 MSample (1 GB) total memory
M2i.xxxx-1GS	Memory upgrade to 1 GSample (2 GB) total memory

Options

Order no.	Option
M2i.xxxx-SH5 (1)	Synchronization Star-Hub for up to 5 cards, only 1 slot width
M2i.xxxx-SH16 (1)	Synchronization Star-Hub for up to 16 cards
M2i.xxxx-SSHM (1)	System-Star-Hub Master for up to 15 cards in the system and up to 17 systems, PCI 32 Bit card, sync cables and extra bracket for clock and trigger distribution included
M2i.xxxx-SSHMe (1)	System-Star-Hub Master for up to 15 cards in the system and up to 17 systems, PCI Express card, sync cables and extra bracket for clock and trigger distribution included
M2i.xxxx-SSHS5 (1)	System-Star-Hub Slave for 5 cards in one system, one slot width all sync cables + bracket included
M2i.xxxx-SSHS16 (1)	System-Star-Hub Slave for 16 cards in system, two slots width, all sync cables + bracket included
M2i.4xxx-dig	Additional synchronous digital inputs (2 per analog channel) including Cab-d40-idc-100
M2i.xxxx-bxio	Option BaseXIO: 8 digital I/O lines usable as asynchronous I/O, timestamp ref-clock and additional external trigger lines, additional bracket with 8 SMB connectors
M2i-upgrade	Upgrade for M2i.xxxx: later installation of option -M2i.xxxx-1GS, -SH5, -SH16 or -bxio

Services

Order no.	
Recal	Recalibration at Spectrum incl. calibration protocol

Amplifiers

Order no.	Bandwidth	Connection	Input Impedance	Coupling	Amplification
SPA.1412 (2)	200 MHz	BNC	1 MOhm	AC/DC	x10/x100 (20/40 dB)
SPA.1411 (2)	200 MHz	BNC	50 Ohm	AC/DC	x10/x100 (20/40 dB)
SPA.1232 (2)	10 MHz	BNC	1 MOhm	AC/DC	x100/x1000 (40/60 dB)
SPA.1231 (2)	10 MHz	BNC	50 Ohm	AC/DC	x100/x1000 (40/60 dB)
Information	External Amplifiers with one channel, BNC/SMA female connections on input and output, manually adjustable offset, manually switchable settings. An external power supply for 100 to 240 VAC is included. Please be sure to order an adapter cable matching the amplifier connector type and matching the connector type for your A/D card input.				

Cables

		Order no.					
for Connections	Length	to BNC male	to BNC female	to SMA male	to SMA female	to SMB female	
Analog/Clock/Trigger	80 cm	Cab-3f-9m-80	Cab-3f-9f-80	Cab-3f-3mA-80	Cab-3f-3fA-80	Cab-3f-3f-80	
Analog/Clock/Trigger	200 cm	Cab-3f-9m-200	Cab-3f-9f-200	Cab-3f-3mA-200	Cab-3f-3fA-200	Cab-3f-3f-200	
Probes (short)	5 cm		Cab-3f-9f-5				
Information	The standard adapter cables are based on RG174 cables and have a nominal attenuation of 0.3 dB/m at 100 MHz.						

		to 2x20 pole IDC	to 40 pole FX2				
Digital signals (option)	100 cm	Cab-d40-idc-100	Cab-d40-d40-100				

Software SBench6

Order no.	
SBench6	Base version included in delivery. Supports standard mode for one card.
SBench6-Pro	Professional version for one card: FIFO mode, export/import, calculation functions
SBench6-Multi	Option multiple cards: Needs SBench6-Pro. Handles multiple synchronized cards in one system.
Volume Licenses	Please ask Spectrum for details.

Software Options

Order no.	
SPc-RServer	Remote Server Software Package - LAN remote access for M2i/M3i/M4i/M4x/M2p cards

(1) : Just one of the options can be installed on a card at a time.
(2) : Third party product with warranty differing from our export conditions. No volume rebate possible.

Technical changes and printing errors possible